

**ESSDERC  
ESSCIRC**

**ESSDERC 2006**

**Proceedings of the  
36<sup>th</sup> European Solid-State Device Research  
Conference**

**Montreux, Switzerland**

**19-21 September 2006**

Organization committee at :



ÉCOLE POLYTECHNIQUE  
FÉDÉRALE DE LAUSANNE

**Michel Declercq**

**Yusuf Leblebici**

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### B2L-J Joint Session on Emerging Devices & Circuits 1

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**Time:** Wednesday, September 20, 2006, 09:50 - 11:10

**Place:** Room A402

**Co-Chairs:** John Long

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**Time:** Wednesday, September 20, 2006, 16:30 - 17:50

**Place:** CIRC Room 1

**Co-Chairs:** Francisca Gamiz; University Granada  
Bill Redman-White

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### **A2L-G High Performance MOSFETs with Innovative SD**

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**Time:** Tuesday, September 19, 2006, 09:50 - 11:10

**Place:** Miles Davis Hall

**Co-Chairs:** Kristin De Meyer; IMEC  
Kazunari Ishimaru; Toshiba

09:50	<b>9133</b>	<b>A High Performance pMOSFET with Two-Step Recessed SiGe-S/D Structure for 32 nm Node and Beyond.....</b>	<b>77</b>
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**A2L-H Transport & Noise**

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**Time:** Tuesday, September 19, 2006, 09:50 - 11:10

**Place:** Room A401

**Co-Chairs:** Henryk Przewlocki; *Institute of Electron Technology*  
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09:50

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**Time:** Tuesday, September 19, 2006, 11:10 - 12:30

**Place:** Miles Davis Hall

**Co-Chairs:** Romuald Beck; *Warsaw University of Technology*  
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**A4L-H MEMS & Sensors**

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**Time:** Tuesday, September 19, 2006, 11:10 - 12:30

**Place:** Room A401

**Co-Chairs:** Jacek Baborowski; *CSEM*  
Herbert Shea; *Université de Neuchâtel*

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**A8L-G CMOS with Novel Gate-Stack Solutions**

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**Time:** Tuesday, September 19, 2006, 16:10 - 17:50

**Place:** Miles Davis Hall

**Co-Chairs:** Arnaud Pouydebasque; *Philips, Crolles*  
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<hr/>			
<b>A8L-H</b>	<b>Advanced Transport Models for Nanoscale Devices</b>		
<b>Time:</b>	<b>Tuesday, September 19, 2006, 16:10 - 17:50</b>		
<b>Place:</b>	<b>Room A401</b>		
<b>Co-Chairs:</b>	<b>Bernd Meinerzhagen; Technische Universitat Braunschweig Massimo Rudan; Universita Di Bologna</b>		
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**A8L-J RF Passive Integration**

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**Time:** Tuesday, September 19, 2006, 16:10 - 17:50

**Place:** Room A402

**Co-Chairs:** Pascal Ancey; *ST Microelectronics*  
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16:10

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**B2L-G Carbon Nanotubes**

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**Time:** Wednesday, September 20, 2006, 09:50 - 11:10

**Place:** Miles Davis Hall

**Co-Chairs:** Jacques Gautier; *LETI*  
Massimo Macucci; *Università di Pisa*

09:50

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**B2L-H Novel Memory**

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**Time: Wednesday, September 20, 2006, 09:50 - 11:10**  
**Place: Room A401**  
**Co-Chairs: Giorgio Baccarani; Università di Bologna**  
**Barbara De Salvo; LETI**

09:50  
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**B4L-G High-K Dielectrics in NVM**

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**Time: Wednesday, September 20, 2006, 11:10 - 12:30**  
**Place: Miles Davis Hall**  
**Co-Chairs: Stephen Hall; The University of Liverpool**  
**Andreas Schenk; ETHZ**

11:10  
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## **B4L-H Fluctuation/Variations**

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**Time:** Wednesday, September 20, 2006, 11:10 - 12:30

**Place:** Room A401

**Co-Chairs:** Luc Harpesslagh; *0*  
Serguei Okhonin; *Innovative Silicon Inc.*

11:10		
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## **B8L-G High Voltage Devices**

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**Time:** Wednesday, September 20, 2006, 16:10 - 17:50

**Place:** Miles Davis Hall

**Co-Chairs:** Joachim Burghartz; *Institute for Microelectronics, Stuttgart*  
Mikael Ostling; *KTH*

16:10		
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		<i>Young Chul Choi, Cornell University; Milan Pophristic, Velox Semiconductor; Lester Eastman, Cornell University</i>	

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## **B8L-H Innovative Injection & Transport Devices**

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**Time:** Wednesday, September 20, 2006, 16:10 - 17:50

**Place:** Room A401

**Co-Chairs:** Emmanuel Dubois; *Université des Sciences et Technologies de Lille*  
Erich Kasper; *Universität Stuttgart*

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		<i>Gregory Lousberg, Hongyu Yu, Benoit Froment, IMEC; Ming-Fu Li, National University of Singapore; Emmanuel Augendre, A. De Keersgieter, C. Demeurisse, Malgorzata Jurczak, Serge Biesemans; IMEC</i>	
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		<i>Ritesh Jhaveri, Jason Woo, UCLA</i>	
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		<i>Clement Charbuillet, STMicroelectronics; Emmanuel Dubois, ISEN; Monfray Stéphane, Pierre Bouillon, Thomas Skotnicki, STMicroelectronics</i>	
17:30	<b>9110</b>	<b>Co-Integration of 2 mV/dec Subthreshold Slope Impact Ionization MOS (I-MOS) with CMOS .....</b>	<b>303</b>
		<i>Frédéric Mayer, CEA; Cyrille Le Royer, Gilles Le Carval, Claude Tabone, Laurent Clavelier, Simon Deleonibus, CEA-LETI</i>	

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## **B8L-J Compact Models & Applications**

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**Time:** Wednesday, September 20, 2006, 16:10 - 17:50

**Place:** Room A402

**Co-Chairs:** Raphael Clerc; *IMEP*  
Kevin McCarthy; *University College Cork, Ireland*

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		<i>Ananda Roy, Yogesh Singh Chauhan, Jean-Michel Sallese, EPFL; Christian Enz, CSEM; Adrian Ionescu, Michel Declercq, EPFL</i>	

16:30			
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	<i>Wolfgang Molzer, Infineon Technologies AG; Thomas Schulz, Infineon Technologies; Wade Xiong, Rinn Cleavelin, Texas Instruments Inc.; Klaus Schrüfer, Infineon Technologies AG; Andrew Marshall, Texas Instruments Inc.; Kenneth Matthews, ATDF Inc.; Josef Sedlmeir, Domagoj Siprak; Infineon Technologies AG</i>		
16:50			
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	<i>Rajiv Joshi, Rouwaida Kanj, Sani Nassif, Donald Plass, Yuen Chan, IBM; Ching-Te Chuang, IBM T.J. Watson Research Center</i>		
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## **C2L-G    Imagers & Light Emitting Devices**

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**Time:**        **Thursday, September 21, 2006, 09:50 - 11:10**

**Place:**       **Miles Davis Hall**

**Co-Chairs:** **Eugenio Cantatore; Philips**  
**Roland Thewes; Infineon**

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10:10			
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**C2L-H Process Integration**

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**Time:** Thursday, September 21, 2006, 09:50 - 11:10**Place:** Room A401**Co-Chairs:** Franck Arnaud; *ST Microelectronics*  
Carlos Mazure; *Soitec, Bernin, France*

09:50

**9353 Highly Reliable Tin/ZrO<sub>2</sub>/TiN 3D Stacked Capacitors for 45 nm Embedded DRAM Technologies..... 343**  
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10:30

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*David Gilmer, J.K. Schaeffer, W.J. Taylor, G. Spencer, D.H. Triyoso, M. Raymond, D. Roan, J. Smith, C. Capasso; R.I. Hegde, Freescale Semiconductor, Inc.*

10:50

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*Emmanuel Augendre, IMEC; Bartek Pawlak, Philips; Stefan Kubicek, Thomas Hoffmann, T. Chiarella, C. Kerner, S. Severi, A. Falepin, J. Ramos, A. De Keersgieter, P. Eyben, D. Vanhaeren, W. Vandervorst, M. Jurczak, P. Absil and S. Biesemans, IMEC*

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**C2L-J Carrier Mobility in Multi-Gate Devices**

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**Time:** Thursday, September 21, 2006, 09:50 - 11:10**Place:** Room A402**Co-Chairs:** Lothar Risch; *Infineon*  
Thomas Skotnicki; *STM*

09:50

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**C4L-G Innovative Multigate Devices**

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**Time:** Thursday, September 21, 2006, 11:10 - 12:30

**Place:** Miles Davis Hall

**Co-Chairs:** Thomas Ernst; *LETI*  
Malgorzata Jurczak; *IMEC*

11:10

**9148 Design Considerations and Comparative Investigation of Ultra-Thin SOI, Double-Gate and Cylindrical Nanowire Fets ..... 371**  
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11:50

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12:10

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**C4L-H High-K & Low-K Dielectrics**

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**Time:** Thursday, September 21, 2006, 11:10 - 12:30

**Place:** Room A401

**Co-Chairs:** Judith Mueller; *Freescale*  
Luca Selmi; *Universita degli Studi di Udine*

11:10

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11:30

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11:50

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12:10		
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## **C8L-G DRAM & Flash Memory**

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**Time:** Thursday, September 21, 2006, 16:10 - 17:50

**Place:** Miles Davis Hall

**Co-Chairs:** Stefan Bengtsson; *Chalmers University of Technology*  
Guido Groeseneken; *IMEC*

16:10		
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16:30		
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16:50		
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17:10		
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17:30		
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## **C8L-H Emerging Materials & Devices**

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**Time:** Thursday, September 21, 2006, 16:10 - 17:50

**Place:** Room A401

**Co-Chairs:** Olof Engström; *LETI*  
Joseph Tringe; *Lawrence Livermore National Laboratory*

16:10		
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16:30		
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17:10			
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17:30			
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<hr/>			
<b>C8L-J</b>	<b>Modeling of Gate Stacks &amp; Dielectrics</b>		
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<b>Time:</b>	<b>Thursday, September 21, 2006, 16:10 - 17:50</b>		
<b>Place:</b>	<b>Room A402</b>		
<b>Co-Chairs:</b>	<b>Herve Jaouen; <i>ST Microelectronics</i> Grasser Tibor; <i>TU Wien</i></b>		
16:10			
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16:30			
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	<i>Andrew Brown, Gareth Roy, Asen Asenov, University of Glasgow</i>		
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