Proceedings of the

ISQED 2009

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International Society for Quality Electronic Design

10th International Symposium on Quality Electronic Design (ISQED 2009)

Session 1A: Aging Aware Design

Session 1B: Robust Circuits

Session 1C: Library & Modeling

Session 1D: Design & Modeling in Emerging Technologies

Session 2A: Circuits for Noise & Variation Tolerance

Session 2B: Power vs Performance Trade-offs

Session 2C: Process Variation

Session 2D: Embedded Papers

Session 3A: System Level Modeling & Design

Session 3B: System & Interface Validation

Session 3C: Quality Digital Design

Session 3D: Embedded Papers

Session 4A: Co Design Applications for IC Packaging

Session 4B: Novel Design Methodologies

Session 4C: Memory Design Solutions

Session 5A: Clock and Noise

Session 5B: Poly Analysis & Delivery Systems

Session 5C: Test Power and Noise

Session 6A: Advances in Timing Analysis & Floor Planning

Session 6B: Low Voltage Design

Session 6C: Low Voltage & Variation Tolerant Design

Session 6D: System Power & Reliability