# 2009 Symposium on VLSI Circuits

## Kyoto, Japan
## 16 – 18 June 2009

**IEEE Catalog Number:** CFP09VLS-PRT
**ISBN:** 978-1-4244-3307-0

******This publication is a representation of what appears in the IEEE Digital Libraries. Some format issues inherent in the e-media version may also appear in this print version.***

IEEE Catalog Number: CFP09VLS-PRT
ISBN 13: 978-1-4244-3307-0

**Additional Copies of This Publication Are Available From:**

Curran Associates, Inc
57 Morehouse Lane
Red Hook, NY 12571 USA
Phone: (845) 758-0400
Fax: (845) 758-2633
E-mail: curran@proceedings.com

# TABLE OF CONTENTS

## SESSION 5 – TRANSMITTERS AND RECEIVERS



## SESSION 6 – BIOMEDICAL AND SENSORS



## SESSION 7 – SIGMA-DELTA ADCS



## SESSION 8 – SPECIAL NON-VOLATILE MEMORIES

## SESSION 9 – DATA INTERCONNECT TECHNOLOGY



## SESSION 10 – PLENARY SESSION II



## SESSION 11 – RELIABILITY AND SECURITY



## SESSION 12 – CLOCK GENERATORS



## SESSION 13 – DRAM AND INTERFACE

## SESSION 18 – FREQUENCY SYNTHESIZERS



## SESSION 19 – POWER MANAGEMENT AND HIGH-EFFICIENCY LOGIC



## SESSION 20 – ANALOG TECHNIQUES



## SESSION 21 – ADVANCED EQUALIZERS

## SESSION 22 – CLOCK GENERATION TECHNIQUES



## SESSION 23 – LOW POWER ADCS



## SESSION 24 – MM-WAVE TECHNIQUES



## SESSION 25 – MEDIA PROCESSOR ARCHITECTURE



## SESSION 26 – HIGH-SPEED ADCS

## SESSION 27 – WIRELESS TRANSCEIVERS



## SESSION 28 – SIGNAL PROCESSING



## SPECIAL SESSION

**No Papers are Available from this Session**