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| TPC CO-Chairs: Kun QIU (UESIC, China)  |         |
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- Track 10. Electronic Design Automation Symposium
- TPC Co-Chairs: Chung-kuan CHENG (UCSD, USA)

# Masahiro FUJITA (TokuoU,Japan)

Margaret Marek-Sadowska (UCSB, USA)

Zhihua WANG (THU, China)

Martin Wong (UIUC, USA)

#### #10\_01\_04: Optimizing Buffer Usage for Networks-on-Chip Design

| Shouyi Yin, Leibo Liu, Shaojun Wei   |        |
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| National Laboratory for Information Science and Technology Institute of Microelectronics,        |        |
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| Tsinghua University Beijing 100084, China  |        |
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| State Key Laboratory of Electronic Films and Integrated Devices, University of Electronic        |        |
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