

# **2011 IEEE Workshop on Microelectronics and Electron Devices**

**(WMED 2011)**

**Boise, Idaho, USA  
22 April 2011**



**IEEE Catalog Number: CFP11564-PRT  
ISBN: 978-1-4244-9740-9**

## TABLE OF CONTENTS

### PROCESS AND DEVICES

<b>Structural Study of Ag-Ge-S Solid Electrolyte Glass System for Resistive Radiation Sensing .....</b>	1
<i>P. Chen, M. Ailavajhala, M. Mitkova, D. Tenne, I. Esqueda, H. Barnaby</i>	
<b>Friction Based Endpoint Technique for Barrier Polish during Copper CMP.....</b>	5
<i>S. Zhu, J. Hofmann, A. Jindal</i>	
<b>Finite Element Modeling of a Back Grinding Process for Through Silicon Vias .....</b>	9
<i>A. Abdelnaby, G. Potirniche, F. Barlow, A. Elshabini, R. Parker</i>	
<b>V<sub>T</sub> Statistics On Nanoscale NAND Flash Arrays .....</b>	13
<i>A. Spessot, A. Calderoni, P. Fantini</i>	
<b>High-Performance Transistor Evaluation for Low-Cost Embedded DRAM .....</b>	17
<i>H. Furusawa, S. Nogami, T. Ogawa, M. Kumazaki, N. Okada, H. Yamamoto</i>	

### SOLID STATE CIRCUITS

<b>Self-calibrating Continuous-time Equalization with Multiple Degrees of Freedom .....</b>	21
<i>T. Hollis</i>	
<b>Adjustable Supply Voltages And Refresh Cycle For Process Variations And Temperature Changing Adaptation In DRAM To Minimize Power Consumption.....</b>	25
<i>L. Tran, F. Kurdahi, A. Eltawil</i>	
<b>All Digital Duty-Cycle Correction Circuit Design And Its Applications In High-Performance DRAM .....</b>	29
<i>F. Lin</i>	
<b>A Non-Volatile Memory Array based on Nano-Ionic Conductive Bridge Memristors.....</b>	33
<i>S. Wald, J. Baker, M. Mitkova, N. Rafla</i>	
<b>Author Index</b>	