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Jian-Jia Chen, Jörg Henkel, Naehyuck Chang and Andy D. Pimentel

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#### Memory and Storage

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Exploiting Set-Level Write Non-Uniformity for Energy-Efficient NVM-Based Hybrid Cache"\$%

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On the Management of Multichannel Architectures of Solid-State Disks"\$' + Li-Pin Chang, Yi-Hsun Huang and Chen-Yi Wen

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Multi-ASIP Based Parallel and Scalable Implementation of Motion Estimation Kernel for High Definition Videos"\$) \* Hong Chinh Doan, Haris Javaid and Sri Parameswaran

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# Automatic Derivation of Polyhedral Process Networks from While-Loop Affine Programs %&

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### **Fault Tolerance and Scheduling**

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## Towards an ESL Design Framework For Adaptive and Fault-Tolerant MPSoCs: MADNESS or not?"%\$\$

Emanuele Cannella, Lorenzo Di Gregorio, Leandro Fiorin, Menno Lindwer, Paolo Meloni, Olaf Neugebauer and Andy Pimentel

# Tractable Real-Time Schedulability Analysis For Mode Changes Under Temporal Isolation "% \$

Nathan Fisher and Masud Ahmed

# Evaluation of Scheduling Heuristics for Jitter Reduction of Real-Time Streaming Applications on Multi-core General Purpose Hardware<sup>\*\*</sup>%

Mark Westmijze, Marco Bekooij, Gerard Smit and Marc Schrijver