2012 Proceedings of the European Solid-State Device Research Conference

(ESSDERC 2012)

Bordeaux, France
17 – 21 September 2012

IEEE

IEEE Catalog Number: CFP12543-PRT
ISBN: 978-1-4673-1707-8

Table of Contents

Table of Contents

Table of Contents

Table of Contents

Table of Contents

A4L-E Variability

Date: Tuesday, September 18, 2012
Time: 15:10 - 16:10
Room: Room E2
Chairs: Hervé JAOUEN; *STMicroelectronics*
Ray Hueting

A4L-F Advanced FETs

Date: Tuesday, September 18, 2012
Time: 15:10 - 16:10
Room: Room F1
Chairs: Giorgio Baccarani; *University of Bologna*
Max Lemme; *KTH*

Table of Contents

Table of Contents

Table of Contents

Table of Contents

Table of Contents

B7L-G Analog/Low Power Devices

Date: Wednesday, September 19, 2012
Time: 16:30 - 18:10
Room: Room F2
Chairs: Kazu Ishimaru; *Toshiba*
Thomas Ernst; *CEA-LETI*

C2L-E Emerging Devices

Date: Thursday, September 20, 2012
Time: 09:40 - 10:40
Room: Room E2
Chairs: Stephen Hall; *University of Liverpool*
Anthony O'Neill; *Newcastle University*

Table of Contents

C2L-F Characterization of Aging and Failure Mechanisms

Date: Thursday, September 20, 2012
Time: 09:40 - 10:40
Room: Room F1
Chairs: Montserrat Nafria; *UAB*
Joachim Burghartz; *Institut für Mikroelektronik Stuttgart*

C3L-F Resistive Memories

Date: Thursday, September 20, 2012
Time: 11:00 - 12:20
Room: Room F1
Chairs: Andrea Lacaita; *Politecnico di Milano*
Fernanda Irrera; *Università Roma La Sapienza*

Table of Contents

C3L-G Quantum Transport

Date: Thursday, September 20, 2012
Time: 11:00 - 12:20
Room: Room F2
Chairs: Denis Rideau; *STMicroelectronics*
David Esseni; *University of Udine*

C6L-E GaN-based Power Switches

Date: Thursday, September 20, 2012
Time: 16:30 - 17:50
Room: Room E2
Chairs: Gaudenzio Meneghesso; *University of Padova*
Tetsuya Suemitsu; *Tohoku University*

Table of Contents

C6L-F Semi-classical Transport

Date: Thursday, September 20, 2012
Time: 16:30 - 17:50
Room: Room F1
Chairs: Bernd Meinerzhagen; *TU Braunschweig*
Tibor Grasser; *Vienna University of Technology*

C6L-G Low Frequency Noise in Next Generation FET Devices

Date: Thursday, September 20, 2012
Time: 16:30 - 17:50
Room: Room F2
Chairs: Paolo Pavan; *Universita degli Studi di Modena e Reggio Emilia*
Gunnar Malm; *KTH Royal Institute of Technology*