

# **2012 Proceedings of the ESSCIRC**

## **(ESSCIRC 2012)**

**Bordeaux, France**  
**17 – 21 September 2012**



**IEEE Catalog Number: CFP12542-PRT**  
**ISBN: 978-1-4673-2212-6**

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Date:	Thursday, September 20, 2012	
Time:	16:30 - 17:50	
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Date:	Thursday, September 20, 2012	
Time:	16:30 - 17:50	
Room:	Room H	
Chairs:	Stefan Rusu; <i>Intel Corp</i> Ralph Hasholzner; <i>Intel Corporation</i>	
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 Time: 16:30 - 17:50  
 Room: Room D  
 Chairs: Peter Mole; *Intersil*  
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<p><b>A CMOS LDO Regulator with High PSR Using Gain Boost-Up and Differential Feed Forward Noise Cancellation in 65nm Process</b>.....</p> <p>Young-Sub Yuk<sup>1</sup>, Seungchul Jung<sup>1</sup>, Byunghun Lee<sup>2</sup>, Se-Won Wang<sup>1</sup>, Chul Kim<sup>3</sup>, Gyu-Hyeong Cho<sup>1</sup></p> <p><sup>1</sup><i>Korea Advanced Institute of Science and Technology, Korea, South</i>; <sup>2</sup><i>Korea Advanced Institute of Science and Technology &amp; Georgia Institute of Technology, Korea, South</i>; <sup>3</sup><i>Korea Advanced Institute of Science and Technology &amp; SK Hynix, Korea, South</i></p>	<p>462</p>
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### C6L-D Nanometer Digital Circuit Techniques

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Date: Thursday, September 20, 2012  
 Time: 16:30 - 17:50  
 Room: Room E1  
 Chairs: Tobias Gemmeke; *IMEC Holst Center*  
 Christian Piguet; *CSEM*

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