

# **2012 18th IEEE-NPSS Real Time Conference**

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# 2012 18<sup>th</sup> IEEE-NPSS Real Time Conference Proceedings

## Open 1 Welcome and Opening Session

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### Open 1-4

(invited) LHC Trigger & DAQ - an Introductory Overview \*\*\*%

N. Neufeld

CERN, Switzerland

## NSET New Standard, Emerging Technologies and TCA

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### NSET-2

Recent Progress in Next-Generation Platform Standards for Physics Instrumentation and Controls \*\*\*)

R. Larsen

SLAC National Accelerator Laboratory, United States

### NSET-3

Scalable SpaceWire Backplane System Using uTCA \*\*\*\$

T. Yuasa<sup>1</sup>, M. Nomachi<sup>2</sup>, T. Takahashi<sup>1</sup>, M. Ioki<sup>3</sup>

<sup>1</sup>Japan Aerospace Exploration Agency, Institute of Space and Astronautical Science, Japan; <sup>2</sup>Osaka University, Japan; <sup>3</sup>Institute for Unmanned Space Experiment Free Flyer, Japan

## RTSA Real Time System Architectures

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### RTSA-1

Auger ACCESS - Remote Monitoring and Controlling the Auger Experiment \*\*\*%

T. Jeikal, on behalf of the Pierre Auger Collaboration

Karlsruhe Institute of Technology, Germany;

### RTSA-3

A System for Monitoring and Tracking the LHC Beam Spot within the ATLAS High Level Trigger \*\*\*&%

R. Bartoldus<sup>1</sup>, J. Cogan<sup>1</sup>, A. Salnikov<sup>1</sup>, E. Strauss<sup>1</sup>, F. Winklmeier<sup>2</sup> on behalf of the ATLAS Collaboration

<sup>1</sup>SLAC, United States; <sup>2</sup>CERN, Switzerland

### RTSA-4

Data Flow and High Level Trigger of Belle II DAQ System \*\*\*&,

R. Itoh<sup>1</sup>, T. Higuchi<sup>1</sup>, M. Nakao<sup>1</sup>, S. Y. Suzuki<sup>1</sup>, S. Lee<sup>2</sup>

<sup>1</sup>KEK, Japan; <sup>2</sup>Korea University, Korea

### RTSA-5

A Prototype Clock System for LHAASO WCDA \*\*\* &

L. Shang<sup>1,2</sup>, K. Song<sup>1,2</sup>, P. Cao<sup>1,2</sup>, C. Li<sup>1,2</sup>, S. Liu<sup>1,2</sup>, Q. An<sup>1,2</sup>

<sup>1</sup>University of Science and Technology of China, China; <sup>2</sup>State Key Laboratory of Particle Detection and Electronics, China

## PS1 Poster Session 1

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### PS1-1

Implementation of an ATCA/AXIe Board for Fast Control and Data Acquisition Systems of Nuclear Fusion

Devices \*\*\* ,

A. J. N. Batista<sup>1</sup>, C. Leong<sup>2</sup>, V. Bexiga<sup>2</sup>, A. P. Rodrigues<sup>1</sup>, A. Combo<sup>1</sup>, B. B. Carvalho<sup>1</sup>, P. Ricardo<sup>1</sup>, J. Fortunato<sup>1</sup>, B. Santos<sup>1</sup>, P. Carvalho<sup>1</sup>, M. Correia<sup>1</sup>, J. P. Teixeira<sup>2</sup>, I. C. Teixeira<sup>2</sup>, J. Sousa<sup>1</sup>, B. Goncalves<sup>1</sup>, C. A. F. Varandas<sup>1</sup>

<sup>1</sup>Instituto Superior Tecnico - Universidade Tecnica de Lisboa, Portugal; <sup>2</sup>INESC-ID, Portugal

### PS1-2

Automatic System-Level Synthesis for Heterogeneous Platforms \*\*\*(&

H. A. Andrade, K. Ravindran

National Instruments Corporation, United States

**PS1-3****Monitoring and Improving the ALICE Data Taking Efficiency**

V. Barroso<sup>1</sup>, F. Carena<sup>1</sup>, W. Carena<sup>1</sup>, S. Chapelard<sup>1</sup>, F. Costa<sup>1</sup>, E. Denes<sup>2</sup>, R. Divia<sup>1</sup>, A. Grigore<sup>3</sup>, G. Simonetti<sup>4</sup>, C. Soos<sup>1</sup>, A. Telesca<sup>1</sup>, P. Vande Vyvre<sup>1</sup>, B. von Haller<sup>1</sup>

<sup>1</sup>CERN, Switzerland; <sup>2</sup>Hungarian Academy of Sciences, Hungary; <sup>3</sup>Polytechnic University of Bucharest, Romania;

<sup>4</sup>Universita Bari, Italy

**PS1-4****Open-Standard Blade Systems Enable High Performance Applications**

S. McClellan<sup>1</sup>, K. Austin<sup>2</sup>, A. Deikman<sup>2</sup>,

<sup>1</sup>Texas State University, United States; <sup>2</sup>ZNYX Networks, United States

**PS1-7****Performance Evaluation of 8-Channel ADC ATCA Card for Direct Sampling of 1.3 GHz Signals**

S. Bou Habib

ISE-WUT/DESY, Poland

**PS1-8****Vector Modulator Card for MTCA-Based LLRF Control System for Linear Accelerators**

J. Rutkowski<sup>1</sup>, K. Czuba<sup>1</sup>, D. Makowski<sup>2</sup>, A. Mielczarek<sup>2</sup>, H. Schlarb<sup>3</sup>, F. Ludwig<sup>3</sup>,

<sup>1</sup>Warsaw University of Technology, Poland; <sup>2</sup>Technical University of Lodz, Poland; <sup>3</sup>Deutsches Elektronen Synchrotron, Germany

**PS1-9****RF Backplane for MTCA.4 Based LLRF Control System**

K. Czuba<sup>1</sup>, M. Hoffmann<sup>2</sup>, T. Jezynski<sup>2</sup>, F. Ludwig<sup>2</sup>, H. Schlarb<sup>2</sup>

<sup>1</sup>Warsaw University of Technology, <sup>2</sup>Poland; DESY, Germany

**PS1-10****Timing Distribution and Synchronization of an ATCA Fast Controller for Fusion Devices**

M. Correia<sup>1</sup>, J. Sousa<sup>1</sup>, B. B. Carvalho<sup>1</sup>, A. Combo<sup>1</sup>, A. P. Rodrigues<sup>1</sup>, A. J. N. Batista<sup>1</sup>, B. Santos<sup>1</sup>, P. R. F. Carvalho<sup>1</sup>, B. Goncalves<sup>1</sup>, C. M. B. A. Correia<sup>2</sup>, C. A. F. Varandas<sup>1</sup>

<sup>1</sup>Instituto Superior Tecnico - Universidade Tecnica de Lisboa, Portugal; <sup>2</sup>Universidade de Coimbra, Portugal

**PS1-11****Intelligent Platform Management Controller Software Architecture in ATCA Modules for Fast Control Systems**

A. P. Rodrigues<sup>1</sup>, M. Correia<sup>1</sup>, A. J. N. Batista<sup>1</sup>, P. R. Carvalho<sup>1</sup>, B. Santos<sup>1</sup>, B. B. Carvalho<sup>1</sup>, J. Sousa<sup>1</sup>, B. Goncalves<sup>1</sup>, C. C. M. B. Correia<sup>2</sup>, C. A. F. Varandas<sup>1</sup>

<sup>1</sup>Instituto Superior Tecnico - Universidade Tecnica de Lisboa, Portugal; <sup>2</sup>Universidade de Coimbra, Portugal

**PS1-12****Firmware Upgrade in xTCA Systems**

D. Makowski<sup>1</sup>, A. Mielczarek<sup>1</sup>, G. Jablonski<sup>1</sup>, P. Predki<sup>1</sup>, T. Jezynski<sup>2</sup>, H. Schlarb<sup>2</sup>, A. Napieralski<sup>1</sup>

<sup>1</sup>Technical University of Lodz, Poland; <sup>2</sup>Deutsche Elektronen-Synchotron, Germany

**PS1-13****Standalone First Level Event Selection Package for the CBM Experiment**

I. Kise<sup>1,2,3</sup>, I. Kulakov<sup>1,3</sup>, M. Zyzak<sup>1,3</sup>

<sup>1</sup>Goethe University Frankfurt, Germany; <sup>2</sup>FIAS Frankfurt Institute for Advanced Studies, Germany; <sup>3</sup>GSI Helmholtzzentrum fuer Schwerionenforschung, Germany

**PS1-14****The XFEL RF Interlock System**

M. Penno<sup>1</sup>, H. Leich<sup>1</sup>, T. Grevesmuehl<sup>2</sup>, C. Rueger<sup>1</sup>, K. Machau<sup>2</sup>

<sup>1</sup>DESY Zeuthen, Germany; <sup>2</sup>DESY Hamburg, Germany

**PS1-15****Development and Calibration of a Real-Time Airborne Radioactivity Monitor Using Gamma-Ray Spectrometry****on a Particulate Filter**

R. Casanova<sup>1</sup>, J. J. Morant, M. Salvado

Universitat Rovira i Virgili, Spain

**PS1-16****Using Data-Oriented Storage Method to Build a High-Parallel and High-Efficiency Disk Cluster**

J. Wu, L. F. Liu, Z. Han, S. Chen, J. Shan, K. Y. Tian, J. Dong  
*University of Sci.&Tech. of China, China, 230026*

**PS1-17****Asynchronous and Synchronous Implementations of the Autocorrelation Function for the FPGA X-Ray Pixel****Array Detector**

M. S. Hromalik<sup>1,2</sup>, K. S. Green<sup>2</sup>, H. T. Philipp<sup>2</sup>, M. T. W. Tate<sup>2</sup>, S. M. Gruner<sup>2,3</sup>

<sup>1</sup>*State University of New York at Oswego, United States*; <sup>2</sup>*Cornell University, United States*; <sup>3</sup>*Cornell High Energy Synchrotron Source (CHESS), United States*

**PS1-18****Real-Time Fast Controller Prototype for J-TEXT Tokamak**

W. Zheng, M. Zhang, G. Zhuang, C. Weng, R. Liu, Y. He, T. Ding, X. Zhang  
*Huazhong University of Science & Technology, China*

**PS1-19****A Dedicated Processor for Monte Carlo Computation in Radiotherapy**

C. Pili<sup>1,2</sup>, V. Fanti<sup>1,2</sup>, G. R. Fois<sup>1,2</sup>, R. Marzeddu<sup>1,2</sup>, P. Randaccio<sup>1,2</sup>, S. Siddhanta<sup>1,2</sup>, J. Spiga<sup>1,2</sup>, A. Szostak<sup>1,2</sup>

<sup>1</sup>*University of Cagliari, Italy*; <sup>2</sup>*INFN Sez. Cagliari, Italy*

**PS1-20****New RFX-Mod Feedback Control System Based on MARTe Real-Time Framework**

G. Manduchi, A. Luchetta, C. Taliercio, A. Soppelsa  
*Consorzio RFX, Italy*

**PS1-21****Real Time FPGA-Based Crosstalk Elimination for Multichannel Interferometry Systems in Fusion Diagnostics**

S. Hernandez-Montero<sup>1</sup>, J. A. Lopez-Martin<sup>1</sup>, M. Sanchez<sup>2</sup>, L. Esteban<sup>2</sup>, *CIEMAT, Spain*

<sup>1</sup>*Universidad Politecnica de Madrid, Spain*; <sup>2</sup>*CIEMAT, Spain*

**PS1-22****A Real-Time Architecture for the Identification of Faulty Magnetic Sensors in the JET Tokamak**

A. C. Neto<sup>1</sup>, D. Alves<sup>1</sup>, B. B. Carvalho<sup>1</sup>, G. De Tommasi<sup>2</sup>, R. Felton<sup>3</sup>, H. Fernandes<sup>1</sup>, P. J. Lomas<sup>3</sup>, F. Maviglia<sup>2</sup>, F. G. Rimini<sup>3</sup>, F. Sartori<sup>4</sup>, A. V. Stephen<sup>3</sup>, D. F. Valcarcel<sup>1</sup>, L. Zabeo<sup>5</sup>

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**PS1-23****Parallel Task Management Library for MARTe**

D. F. Valcarcel<sup>1</sup>, D. Alves<sup>1</sup>, A. Neto<sup>1</sup>, C. Reux<sup>2</sup>, B. B. Carvalho<sup>1</sup>, R. Felton<sup>3</sup>, P. J. Lomas<sup>3</sup>, J. Sousa<sup>1</sup>, L. Zabeo<sup>4</sup>, *JET EFDA Contributors\**<sup>5</sup>

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<sup>4</sup>*ITER Organisation, France*; <sup>5</sup>*JET-EFDA, Culham Science Centre, UK*

**PS1-24****A Real-Time Data Transmission Method Based on Linux for Physical Experimental Readout Systems**

P. Cao<sup>1,2</sup>, K. Song<sup>1,2</sup>, J. Yang<sup>1,2</sup>, K. Zhang<sup>1,2</sup>

<sup>1</sup>*State Key Laboratory of Particle Detection and Electronics, China*; <sup>2</sup>*University of Science and Technology of China, China*

**PS1-25****A Single-FPGA Real-time Dual Beam-Former with FX Correlation Capabilities. First Results at the Nançay****Radio Telescope with the FAN Antenna Array**

H. Deschamps<sup>1</sup>, C. Viou<sup>2</sup>, J. Pezzani<sup>2</sup>, P. Abbon<sup>1</sup>, R. Ansari<sup>2</sup>, C. Beigbeder<sup>2</sup>, D. Breton<sup>2</sup>, T. Cacérès<sup>2</sup>, D. Charlet<sup>2</sup>, C. Flouzat<sup>1</sup>, P. Kestener<sup>1</sup>, C. Magneville<sup>1</sup>, B. Mansoux<sup>2</sup>, C. Pailler<sup>2</sup>, M. Taurigna<sup>2</sup>, and C. Yèche<sup>1</sup>

<sup>1</sup>*Comissariat a l'Energie Atomique, IRFU, France*; <sup>2</sup>*Observatoire de Paris, Unité Scientifique de Nançay, France*

<sup>2</sup>*CNRS-IN2P3-LAL, France*

**PS1-26****A Two-Stage Distributed Architecture Designed for DAQ of Thousands-Channel Physical Experiment**K. Song<sup>1,2</sup>, P. Cao<sup>1,2</sup>, J. Yang<sup>1,2</sup><sup>1</sup>*University of Science & Technology of China, China;* <sup>2</sup>*State Key Laboratory of Particle Detection and Electronics, China***PS1-27****An Application Using MicroTCA for Real-Time Event Assembly**R.A. Rivera,*Fermilab, United States***PS1-28****Digital Programmable Emulator and Analyzer of Radiation Detection Setups**A. Geraci, A. Abba, F. Caponio*Politecnico di Milano, Italy***PS1-30****Ultra-Fast Streaming Camera Platform for Scientific Applications**M. Caselle, M. Balzer, S. Chilingaryan, A. Herth, A. Kopmann, U. Stevanovic, M. Vogelgesang*Karlsruhe Institute of Technology, Germany***PS1-31****LHCb Online in the Cloud: Off-site Computing Resources for the LHCb High Level Trigger**G. Liu, N. Neufeld,*CERN, Switzerland***PS1-32****A New Generation of Real-Time Systems in the JET Tokamak**D. M. Alves<sup>1</sup>, A. C. Neto<sup>1</sup>, D. F. Valcrel<sup>1</sup>, R. Felton<sup>2</sup>, J. M. Lopez<sup>3</sup>, A. Barbalace<sup>4</sup>, L. Boncagni<sup>5</sup>, P. Card<sup>2</sup>, A. Goodyear<sup>2</sup>, S. Jachmich<sup>6,7</sup>, P. J. Lomas<sup>2</sup>, F. Maviglia<sup>8</sup>, P. A. McCullen<sup>2</sup>, A. Murari<sup>4</sup>, M. Rainford<sup>2</sup>, C. Reux<sup>9</sup>, F. Rimini<sup>2</sup>, F. Sartori<sup>10</sup>, A. V. Stephen<sup>2</sup>, J. Vega<sup>11</sup>, R. Vitelli<sup>12</sup>, L. Zabeo<sup>13</sup>, K.-D. Zastrow<sup>2</sup><sup>1</sup>*Associao EURATOM/IST, Instituto de Plasmas e Fusio Nuclear - Laboratrio Associado, Portugal;* <sup>2</sup>*EURATOM/CCFE Fusion Association, Culham Science Centre, Abingdon, Oxon, OX14 3DB, United Kingdom;* <sup>3</sup>*CAEND. Universidad**Politcnica de Madrid, Spain;* <sup>4</sup>*Associazione EURATOM-ENEA sulla Fusione, Consorzio RFX, Padova, Italy, Italy;* <sup>5</sup>*Associazione EURATOM/ENEA, 00040 Frascati, Italy, Italy;* <sup>6</sup>*Laboratory for Plasma Physics, Ecole Royale Militaire/Koninklijke Militaire School, EURATOM-Associat, Belgium;* <sup>7</sup>*EFDA-CSU, Culham Science Centre, Abingdon, OX14 3DB, UK, United Kingdom;* <sup>8</sup>*Associazione EURATOM-ENEA-CREATE, Univ. di Napoli Federico II, Via Claudio 21, 80125, Napoli, Italy, Italy;* <sup>9</sup>*Ecole Polytechnique, LPP, CNRS UMR 7648, 91128 Palaiseau, France, France;*<sup>10</sup>*Fusion for Energy, 08019 Barcelona, Spain, Spain;* <sup>11</sup>*Laboratorio Nacional de Fusion, Asociacion EURATOM-CIEMAT, Madrid, Spain, Spain;* <sup>12</sup>*Dipartimento di Informatica, Sistemi e Produzione, Universit di Roma Tor Vergata 00133 Rome, Italy, Italy;* <sup>13</sup>*ITER, St. Paul-Lez-Durance 13108, France, France*

## TRG Triggers

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**TRG-2****The ALICE High Level Trigger: the 2011 Run Experience.**

T. Kollegger

*FIAS/University of Frankfurt, Germany***TRG-3****The evolution and performance of the ATLAS calorimeter-based triggers in 2011 and 2012**I. Radoslavova Hristova<sup>2</sup>, on behalf of the ATLAS Collaboration*Humboldt-Universitaet zu Berlin, Germany***TRG-4****Use of Expert System and Data Analysis Technologies in Automation of Error Detection, Diagnosis and****Recovery for ATLAS Trigger-DAQ Controls Framework**A. Kazarov<sup>1</sup>, G. Lehmann Miotto<sup>2</sup>, L. Magnoni<sup>2</sup>, A. Corso Radu<sup>3</sup><sup>1</sup>*Petersburg Nuclear Physics Institute, <sup>2</sup>NRC Kurchatov Institute, Russia; CERN, Switzerland;* <sup>3</sup>*University of California Irvine, USA*

## TRG-5

**Evolution and Performance of the ATLAS Trigger System with p-p Collisions at 7 TeV** \*\*\*&%

T. Kono

*IFAE, Spain*

## TRG-6

**Recent Experience and Future Evolution of the CMS High Level Trigger System** \*\*\*&%

A. C. Spataru<sup>1</sup>, G. Bauer<sup>2</sup>, U. Behrens<sup>3</sup>, J. Branson<sup>4</sup>, S. Bukowiec<sup>1</sup>, O. Chaze<sup>1</sup>, S. Cittolin<sup>5,4</sup>, J. A. Coarasa<sup>1</sup>, C. Deldicque<sup>1</sup>, M. Dobson<sup>1</sup>, A. Dupont<sup>1</sup>, S. Erhan<sup>4</sup>, D. Gigi<sup>1</sup>, F. Glege<sup>1</sup>, R. Gomez-Reino<sup>1</sup>, C. Hartl<sup>1</sup>, A. Holzner<sup>4</sup>, L. Masetti<sup>1</sup>, F. Meijers<sup>1</sup>, E. Meschi<sup>1</sup>, R. K. Mommsen<sup>6</sup>, C. Nunez-Barranco-Fernandez<sup>1</sup>, V. O'Dell<sup>6</sup>, L. Orsini<sup>1</sup>, C. Paus<sup>2</sup>, A. Petrucci<sup>1</sup>, M. Pieri<sup>4</sup>, G. Polese<sup>1</sup>, A. Racz<sup>1</sup>, O. Raginel<sup>2</sup>, H. Sakulin<sup>1</sup>, M. Sani<sup>4</sup>, C. Schwick<sup>1</sup>, F. Stoeckli<sup>2</sup>, K. Sumorok<sup>2</sup>

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## MSP1 Monitoring and Signal Processing 1

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### MSP1-1

**Novel, Highly-Parallel Software for the Online Storage System of the ATLAS Experiment at CERN: Design and**

**Performances** \*\*\*&%

T. Colombo<sup>1,2</sup>, W. Vandelli<sup>1</sup>

*<sup>1</sup>CERN, Switzerland; <sup>2</sup>Università' di Pavia, Italy*

### MSP1-2

**Advanced Visualization System for Monitoring the ATLAS TDAQ Network in Real-Time** \*\*\*&%

S. Batraneanu<sup>1</sup>, D. Campora Perez<sup>2</sup>, B. Martin<sup>3</sup>, D. Savu<sup>3</sup>, S. Stancu<sup>3</sup>, L. Leahu<sup>4</sup>

<sup>1</sup>University of California, Irvine, United States; <sup>2</sup>University of Seville, Spain; <sup>3</sup>CERN, Switzerland; <sup>4</sup>Politehnica University Bucharest, Romania

### MSP1-4

**Architecture and Operation of the Control System for ALICE Detector at CERN** \*\*\*& +

P. Chochula

*CERN, Switzerland*

## MSP2 Monitoring and Signal Processing 2

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### MSP2-1

**artdaq: An Event Filtering Framework for Fermilab Experiments** \*\*\*& '

K. Biery, C. Green, J. Kowalkowski, M. Paterno, R. Rechenmacher

*Fermi National Accelerator Lab, United States*

### MSP2-2

**FPGA/NIOS Implementation of an Adaptive FIR Filter Using Linear Prediction to Reduce Narrow-Band RFI for**

**Radio Detection of Cosmic Rays** \*\*\*&%

Z. Szadkowski<sup>1</sup>, D. Fraenkel<sup>2</sup>, A. M. van den Berg<sup>2</sup>

<sup>1</sup>University of Lodz, Poland; <sup>2</sup>University of Groningen, Netherlands

### MSP2-3

**FPGA-Based Algorithm for Center of Gravity Calculation of Clustered Signals** \*\*\*& -

A. A. Ushakov<sup>1</sup>, B. Mindur<sup>2</sup>, T. Fiutowski<sup>2</sup>, C. Schulz<sup>1</sup>, F. Winklmeier<sup>1</sup>

<sup>1</sup>Helmholtz-Zentrum Berlin, Germany; <sup>2</sup>AGH University for Science and Technology, Poland

## UPG1 Upgrades 1

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### UPG1-1

**MEP V2, the New Event Building Protocol for the Upgraded LHCb Experiment** \*\*\*& '

R. Schwemmer, N. Neufeld, G. Liu,

*CERN, Switzerland*

### UPG1-2

**A New Readout Control System for the LHCb Upgrade at CERN** \*\*\*&\* +

F. Alessio, R. Jacobsson,

*CERN, Switzerland*

### UPG1-3

#### Topological and Central Trigger Processor for 2014 LHC luminosities \*\*\*&\*

J. T. Childers, G. Anders, B. Bauss, D. Berge, V. Bouscher, R. Degele, E. Dobson, A. Ebling, N. Ellis, P. Farthouat, C. Gabaldon, B. Gorini, S. Haas, W. Ji, M. Kaneda, S. Maettig, A. Messina, C. Meyer, S. Moritz, T. Pauly, R. Pottgen, U. Schlafer, E. Simioni, R. Spiwoks, S. Tapprogge, T. Wengler, V. Wengler  
CERN, Switzerland

## PS2 Poster Session 2

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### PS2-1

#### High Performance FPGA-Based DMA Interface for PCIe \*\*& %

H. Kavianipour, S. Muschter, C. Bohm  
Stockholm University, Sweden

### PS2-2

#### Low Power, Accurate Time Synchronization MAC Protocol for Real-Time Wireless Data Acquisition \*\*& (

J. Zhang, J. Wu, Z. Han, L. Liu, K. Tian  
University of Science and Technology of China, China

### PS2-3

#### A MAC Layer Congestion Control Method to Achieve High Network Performance for EAST Tokamak \*\*\*& -

K. Shi<sup>1,2</sup>, Y. Shu<sup>2</sup>, S. Lin<sup>1</sup>, J. Luo<sup>3</sup>

<sup>1</sup>Tianjin University of Technology, China; <sup>2</sup>Tianjin University, China; <sup>3</sup>Academia Sinica, China

### PS2-5

#### Modulator-Based, High Bandwidth Optical Links for HEP Experiments \*\*& '

W. S. Fernando, R. W. Stanek, D. G. Underwood, D. Lopez  
Argonne National Lab, United States

### PS2-6

#### Waveform Timing Algorithms with a 5 GS/s Fast Pulse Sampling Module \*\*& ,

J. Wang<sup>1,2</sup>, L. Zhao<sup>1,2</sup>, C. Feng<sup>1,2</sup>, Y. Zhang<sup>1,2</sup>, S. Liu<sup>1,2</sup>, Q. An<sup>1,2</sup>

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### PS2-7

#### The Study of Multi-Channel High Precision Pulse Synchronizer \*\*\* \$&

F. Li<sup>1,2</sup>, L. Chen<sup>1,2</sup>, F. Liang<sup>1,2</sup>, G. Jin<sup>1,2</sup>

<sup>1</sup>University of Science and Technology of China, China; <sup>2</sup>State Key Laboratory of Particle Detection and Electronics, China

### PS2-8

#### Sophisticated Online Analysis in ADC Boards \*\*\* \$\*

P. Wuestner<sup>1</sup>, A. Erven<sup>1</sup>, W. Erven<sup>1</sup>, G. Kemmerling<sup>1</sup>, H. Kleines<sup>1</sup>, P. Kulessa<sup>2</sup>, P. Marciniewski<sup>3</sup>, H. Ohm<sup>1</sup>, K. Pysz<sup>1</sup>, V. Serdyuk<sup>1</sup>, S. van Waasen<sup>1</sup>, P. Wintz<sup>1</sup>

<sup>1</sup>Research Centre Juelich, Germany; <sup>2</sup>Institute of Nuclear Physiks PAN, Poland; <sup>3</sup>Uppsala University, Sweden

### PS2-9

#### A High Density Time-to-Digital Converter Prototype Module for BESIII End-Cap TOF Upgrade \*\*\* \$-

H. Fan, C. Feng, W. Sun, C. Yin, S. Liu, Q. An  
University of Science and Technology of China, China

### PS2-10

#### Development of White Rabbit Interface for Synchronous Data Acquisition and Timing Control \*\*\* %

Q. Du, G. Gong, W. Pan, H. Lu  
Tsinghua University, China

### PS2-11

#### A High-Resolution Time-to-Digital Converter Based on Multi-Phase Clock Implement in Field-Programmable-Gate-Array \*\*\* %

Z. Yin, S. Liu, X. Hao, S. Gao, Q. An  
University of Science and Technology of China, China

**PS2-12****Real-Time Data Analysis Using the WaveDREAM Data Acquisition System** \*\*\* &\$H. Friederich<sup>1,2</sup>, G. Davatz<sup>1,2</sup>, U. Gendotti<sup>1</sup>, H. Meyer<sup>1</sup>, D. Murer<sup>1,2</sup><sup>1</sup>*Aktis Radiation Detectors Ltd, Switzerland; <sup>2</sup>ETH Zurich, Institute for Particle Physics, Switzerland***PS2-13****DSP Based Smart Sensorless Stepping Motor Driver for LHC Collimators** \*\*\* &+A. Masi, M. Butcher, R. Losito, R. Picatoste Ruilope*CERN, Switzerland***PS2-14****High Accuracy Reading Algorithm for Ironless Linear Position Sensor** \*\*\* (A. Masi, A. Danisi, M. Di Castro, R. Losito*CERN, Switzerland***PS2-15****VHDL Design of Digital Adaptive Filters for PANDA Signal Processing** \*\*\* ( &M. Greco, M. P. Bussa, M. Destefanis, M. Maggiora, S. Spataro, *University of Torino and INFN, Italy***PS2-16****Experience with the Custom-Developed ATLAS Trigger Monitoring and Reprocessing Infrastructure** \*\*\* (\*)V. Bartsch<sup>1</sup>, S. George<sup>2</sup>, M. zur Nedden<sup>3</sup><sup>1</sup>*University of Sussex, United Kingdom; <sup>2</sup>Royal Holloway University of London, United Kingdom; <sup>3</sup>Humboldt-Universitaet zu Berlin, Germany***PS2-17****Optimization of the detection of very inclined showers using a spectral DCT trigger in arrays of surface****detectors** \*\*\* (-Z. Szadkowski, *University of Lodz, Poland***PS2-18****Data Formatter System for the ATLAS Fast TracKer** \*\*\* ) \*J. Olsen<sup>1</sup>, T. Liu<sup>1</sup>, B. Penning<sup>1</sup>, H. L. Li<sup>2</sup><sup>1</sup>*Fermi National Accelerator Laboratory, United States; <sup>2</sup>University of Chicago, United States***PS2-20****Commissioning and Performance of a Fast Level-2 Trigger System at VERITAS** \*\*\* \*\$B. Zitzer<sup>1</sup>, A. Weinstein<sup>2</sup>, M. Schroedter<sup>3</sup>, M. Orr<sup>3</sup>, M. Oberling<sup>1</sup>, A. Kreps<sup>1</sup>, F. Krennrich<sup>2</sup>, G. Drake<sup>1</sup>, K. Byrum<sup>1</sup>, J. T. Anderson<sup>1</sup><sup>1</sup>*Argonne National Laboratory, United States; <sup>2</sup>Iowa State University, United States; <sup>3</sup>Smithsonian Astrophysical Observatory, United States***PS2-22****The ATLAS Muon Trigger Performance in Proton-Proton Collisions at Sqrt(s)=7 TeV** \*\*\* \*)K. Nagano<sup>1</sup>, K. Black<sup>2</sup>, T. Matsushita<sup>3</sup><sup>1</sup>*KEK, Japan; <sup>2</sup>Boston University, USA; <sup>3</sup>Kobe University, Japan***PS2-24****Multifunction-Timing Card ITTEV2 for CoDaC Systems of Wendelstein 7-X** \*\*\* +'J. Schacht<sup>1</sup>, J. Skodzik<sup>2</sup><sup>1</sup>*Max-Planck-Institute for Plasmaphysics, Germany; <sup>2</sup>University Rostock, Germany***PS2-25****The ATLAS Jet Trigger** \*\*\* +-M. Campanelli<sup>1</sup>, L. Lopes<sup>2</sup><sup>1</sup>*University College London, United Kingdom; <sup>2</sup>Laboratorio de Instrumentacao e Fisica Experimental de Particulas (PT), Portugal***PS2-27****Development of the Control Card for the Digitizers of the Second Generation Electronics of AGATA** \*\*\* , (D. Barrientos<sup>1,2,3</sup>, V. Gonzalez<sup>3</sup>, M. Bellato<sup>2</sup>, A. Gadea<sup>1</sup>, D. Bazzacco<sup>2</sup>, J. M. Blasco<sup>3</sup>, D. Bortolato<sup>2</sup>, F. J. Egea<sup>1,3</sup>, R. Isocrate<sup>2</sup>, A. Pullia<sup>4</sup>, G. Rampazzo<sup>2</sup>, E. Sanchis<sup>3</sup>, A. Triassi<sup>2</sup><sup>1</sup>*Instituto de Fisica Corpuscular (CSIC-UV), Spain; <sup>2</sup>Istituto di Fisica Nucleare (INFN), Sezione di Padova, Italy;*<sup>3</sup>*Departamento Ingeniera Electronica, Universitat de Valencia, Spain; <sup>4</sup>Istituto di Fisica Nucleare (INFN), Sezione di Milano, Italy*

**PS2-28**

**FPGA Implementation of the 32-Point DFT for a Wavelet Trigger of Cosmic Rays Experiments** "", +  
Z. Szadkowski, *University of Lodz, Poland*

**PS2-29**

**Evolution and Performance of Electron and Photon Triggers in ATLAS in the Year 2011** "" -)  
A. Tricoli<sup>1</sup>, T. Kono<sup>2</sup>, V. Solovyev<sup>3</sup>

<sup>1</sup>CERN, Switzerland; <sup>2</sup>DESY, Germany; <sup>3</sup>B.P. Konstantinov Petersburg Nuclear Physics Institute, Russia

**PS2-30**

**Advanced Light Source Control System Upgrade Intelligent Local Controller Redesign** "" (\$\$)  
E. Norum  
*Lawrence Berkeley National Laboratory, USA*

## DAQ1 Data Acquisition 1 / Medical Imaging

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**DAQ1-1**

**(invited) The Trend of Data Path Structures for Data Acquisition Systems (DAQ) in Positron Emission Tomography (PET) Systems** ""(\$

E. Kim, P. D. Olcott, K. J. Hong, J. Y. Yeom, C. S. Levin

*Stanford University, USA*

**DAQ1-2**

**Design of a real-time FPGA-based DAQ architecture for the LabPET II, an APD-based Scanner dedicated to small animal PET imaging** ""(%  
L. Njeimana, M.-A. Tetraut, L. Arpin, A. Burghgraeve, P. Maille, J.-C. Lavoie, C. Paulin, K. C. Koua, H. Bouziri, S. Panier, M. W. Ben Attouch, M. Abidi, J.-F. Pratte, R. Lecomte, R. Fontaine

*Universite de Sherbrooke, Canada*

**DAQ1-3**

**A Building Block for Nuclear Medicine Imaging Systems Data Acquisition** ""(%

T. K. Lewellen, R. S. Miyaoka, D. DeWitt, S. Hauck

*University of Washington, United States*

**DAQ1-4**

**3D Ultrasound Computer Tomography for Breast Cancer Diagnosis** ""(&\$

M. Balzer, M. Birk, R. Dapp, A. Menshikov, M. Zapf, H. Gemmeke, N. Ruiter

*KIT, Germany*

**DAQ1-5**

**FPGA-Based Multi-Channel DAQ Systems with External PCI Express Link to GPU Compute Servers** ""(&

T. Bergmann<sup>1</sup>, D. Bormann<sup>1</sup>, M. A. Howe<sup>2</sup>, M. Kleifges<sup>1</sup>, A. Kopmann<sup>1</sup>, N. Kunka<sup>1</sup>, A. Menshikov<sup>1</sup>, D. Tcherniakhovski<sup>1</sup>

<sup>1</sup>Karlsruhe Institute of Technology, Germany; <sup>2</sup>University of North Carolina, USA

**DAQ1-6**

**Field-Programmable Gate Array (FPGA) Firmware for the Fermilab E906 (SeaQuest) Trigger** ""(&-

J. Wu<sup>1</sup>, S.-H. Shiu<sup>2</sup>

<sup>1</sup>FNAL, USA; <sup>2</sup>Institute of Physics, Academia Sinica, Taiwan

## UPG2 Upgrades 2

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**UPG2-1**

**The Generic Evaluation Tool for the LHCb Event Builder Network Upgrade** ""(''

G. Liu, N. Neufeld

*CERN, Switzerland*

**UPG2-2**

**Upgrade Project and Plans for the ATLAS Detector and Trigger** ""(' +

F. Pastore, R. Vari

*Royal Holloway University of London, United Kingdom*

**UPG2-3**

**Associative Memories for L1 Track Triggering in LHC Environment** ""(( &

D. Magalotti<sup>1</sup>, E. Pedreschi<sup>1</sup>, A. Annovi<sup>1</sup>, P. Giannetti<sup>1</sup>, M. Piendibene<sup>1,2</sup>, G. Broccolo<sup>3</sup>, F. Palla<sup>1,3</sup>, R. Dell'Orso<sup>1</sup>, F. Ligabue<sup>3</sup>, S. Taroni<sup>1,4</sup>, L. Servoli<sup>1</sup>, A. Nappi<sup>1,4</sup>

<sup>1</sup>INFN, Italy; <sup>2</sup>Universit di Pisa, Italy; <sup>3</sup>Scuola Normale Superiore, Italy; <sup>4</sup>Universita' degli studi di Perugia, Italy

## DAQ2 Data Acquisition 2 / Fusion

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### DAQ2-2

**Feedforward Power Distortion Correction in RF Power Delivery Systems for Plasma Processing Systems** \*\*\*(( ,  
D. J. Coumou,

*MKS, ENI Products, United States*

### DAQ2-3

**Prototyping Control and Data Acquisition for the ITER Neutral Beam Test Facility** \*\*\*(' )'

A. Luchetta<sup>1</sup>, G. Manduchi<sup>1</sup>, A. Soppelsa<sup>1</sup>, C. Taliercio<sup>1</sup>, F. Paolucci<sup>2</sup>, F. Sartori<sup>2</sup>, P. Barbato<sup>1</sup>, M. Breda<sup>1</sup>, R. Capobianco<sup>1</sup>, F. Molon<sup>1</sup>, M. Moressa<sup>1</sup>, S. Polato<sup>1</sup>, P. Simionato<sup>1</sup>, E. Zampiva<sup>1</sup>

<sup>1</sup>*Consorzio RFX - CNR, Italy;* <sup>2</sup>*Fusion for Energy, Spain*

### DAQ2-4

**Real-Time Processing System for the JET Hard X-Ray and Gamma-Ray Profile Monitor Enhancement** \*\*\*(\* \$

A. M. Fernandes<sup>1</sup>, R. C. Pereira<sup>1</sup>, A. Neto<sup>1</sup>, D. F. Valcarcel<sup>1</sup>, J. Sousa<sup>1</sup>, B. B. Carvalho<sup>1</sup>, V. Kiptily<sup>2</sup>, B. Syme<sup>2</sup>, P. Blanchard<sup>3</sup>, A. Murari<sup>4</sup>, C. M. B. A. Correia<sup>5</sup>, C. A. F. Varandas<sup>1</sup>, JET-EFDA Contributors<sup>6</sup>

<sup>1</sup>*Instituto Superior Tecnico, Universidade Técnica de Lisboa, Portugal;* <sup>2</sup>*Culham Science Centre, UK;* <sup>3</sup>*Ecole Polytechnique Federale de Lausanne (EPFL), CRPP, Switzerland;* <sup>4</sup>*Consorzio RFX, Italy;* <sup>5</sup>*Dept. de Fisica, Universidade de Coimbra, Portugal;* <sup>6</sup>*See the Appendix of F. Romanelli et al., Proceedings of the 23rd IAEA Fusion Energy Conference 2010, Korea*

### DAQ2-5

**Study of Radiation Damage in Front-End Electronics Components** \*\*\*(\* +

T. Higuchi<sup>1</sup>, M. Nakao<sup>1</sup>, R. Itoh<sup>1</sup>, S. Y. Suzuki<sup>1</sup>, E. Nakano<sup>2</sup>

<sup>1</sup>*High Energy Accelerator Research Organization, Japan;* <sup>2</sup>*Osaka City University, Japan*

### DAQ2-6

**Readout Hardware and Firmware Architecture of the HFT PXL Detector at STAR** \*\*\*(+ %

J. Schambach<sup>1</sup>, L. Greiner<sup>2</sup>, T. Stezelberger<sup>2</sup>, X. Sun<sup>2</sup>, M. Szelezniak<sup>2,3</sup>, C. Vu<sup>2</sup>

<sup>1</sup>*University of Texas at Austin, United States;* <sup>2</sup>*Lawrence Berkeley National Laboratory, United States;* <sup>3</sup>*IPHC (Institut Pluridisciplinaire Hubert Curien), France*

## DAQ3 Data Acquisition 3

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### DAQ3-1

**The Belle II Pixel Detector Data Acquisition and Reduction System** \*\*\*(+ \*

B. Spruck<sup>1</sup>, T. Gessler<sup>1</sup>, W. Kuehn<sup>1</sup>, S. Lange<sup>1</sup>, H. Lin<sup>2</sup>, Z. Liu<sup>2</sup>, D. Muenchow<sup>1</sup>, H. Xu<sup>1,2</sup>, J. Zhao<sup>2</sup>

<sup>1</sup>*University Giessen, Germany;* <sup>2</sup>*Institute of High Energy Physics, China*

### DAQ3-2

**Design Concepts for a Hierarchical Synchronized Data Acquisition Network for CBM** \*\*\*( , %

E. Lemke, U. Bruening,

*University of Heidelberg, Germany*

## DAQ4 Data Acquisition 4

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### DAQ4-1

**Extending the IceCube DAQ System by Integration of the Generic, High-Speed Sorter Module TESS** \*\*\*( , ,

C. C. W. Robson<sup>1</sup>, K. Hanson<sup>2</sup>

<sup>1</sup>*Stockholms Universitet, Sweden;* <sup>2</sup>*Université Libre de Bruxelles, Belgium*

### DAQ4-2

**Readout of GEM Stacks with the CERN SRS System** \*\*\*(- &

M. L. Purschke

*Brookhaven National Laboratory, United States*

## FERT1 FPGA and Electronics Applied to Realtime Systems 1

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### FERT1-1

**Real-time measurement and adjustment of random phase in frequency-nondegenerate entanglement swapping experiment** \*\*\*(- )

Z. Sang<sup>1,2</sup>, X. Jiang<sup>2,3</sup>, F. Li<sup>1,2</sup>, H. Zhang<sup>2,3</sup>, T. Zhao<sup>2,3</sup>, G. Jin<sup>1,2</sup>

<sup>1</sup>*State Key Laboratory of Particle Detection and Electronics, China;* <sup>2</sup>*University of Science and Technology of China, China;* <sup>3</sup>*Hefei National Laboratory for Physical Science at Microscale, China*

## FERT1-2

### A Compact Dosimeter for Space Applications (\*) \$%

C. Deneau<sup>1</sup>, J.-R. Vaille<sup>1,2</sup>, F. Bezerra<sup>3</sup>, E. Lorfevre<sup>3</sup>, R. Ecoffet<sup>3</sup>, L. Dusseau<sup>1</sup>

<sup>1</sup>Universite Montpellier 2, France; <sup>2</sup>Universite de Nimes, France; <sup>3</sup>Centre National d'Etudes Spatiales, France

## FERT1-3

### A Low-Resolution, GSa/s Streaming Digitizer for a Correlation-Based Trigger System (\*) \$\*

K. Nishimura<sup>1</sup>, M. Andrew<sup>1</sup>, Z. Cao<sup>1</sup>, M. Cooney<sup>1</sup>, P. Gorham<sup>1</sup>, L. Macchiarulo<sup>1</sup>, L. Ritter<sup>1</sup>, A. Romero-Wolf<sup>2</sup>, G. Varner<sup>1</sup>

<sup>1</sup>University of Hawaii at Manoa, United States; <sup>2</sup>Jet Propulsion Laboratory, United States

## PS3 Poster Session 3

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### PS3-1

#### Readout Electronics and Data Acquisition of a Time of Flight Detector for Positron Emission Tomography (\*) %&

J. Y. Yeom, V. Spanoudaki, K. J. Hong, C. S. Levin

Stanford University, United States

### PS3-2

#### A Prototype of Underground Muon Counters Triggered from the Water Cherenkov Surface Detectors Built on Unified Altera Platform (\*) %

Z. Szadkowski,

University of Lodz, Poland

### PS3-3

#### Design of the Trigger Interface and Distribution Board for CEBAF 12 GeV Upgrade (\*) &

W. Gu, D. Abbott, C. Cuevas, G. Heyes, E. Jastrzembski, B. Moffit, B. Raydo, J. Wilson, H. Dong, S. Kaneta, N. Nganga, C. Timmer, V. Gyurjyan

Jefferson Lab, United States

### PS3-4

#### A Correlation Measurement System for Ghost Imaging Experiment (\*) \$

L. Chen<sup>1,2</sup>, M. Zheng<sup>1,2</sup>, L. Zhang<sup>1,2</sup>, G. Jin<sup>1,2</sup>

<sup>1</sup>University of Science and Technology of China, China; <sup>2</sup>State Key Laboratory of Particle Detection and Electronics, China

### PS3-5

#### Design and Implementation of DAQ Readout System for Daya Bay Reactor Neutrino Experiment (\*) ''

X. Ji, F. Li, K. Zhu

Institute of High Energy Physics, Chinese Academy of Sciences, China

### PS3-6

#### ATLAS IBL BOC Prototype Evaluation (\*) ,

N. Schroer,

ZITI - University of Heidelberg, Germany

### PS3-8

#### Clock Distribution Board for the $4\pi\beta\gamma$ Coincidence Counting System (\*) (

H. Wang<sup>1,2</sup>, K. Song<sup>1,2</sup>, J. Yang<sup>1,2</sup>, P. Cao<sup>1,2</sup>, K. Zhang<sup>1,2</sup>

<sup>1</sup>University of Science and Technology of China, China; <sup>2</sup>the State Key Laboratory of Particle Detection and Electronics, China

### PS3-9

#### Implementation of High-Speed USB Interface in Data Acquisition System for KTX (\*) (+

W. Lv<sup>1,2</sup>, K. Song<sup>1,2</sup>, J. Yang<sup>1,2</sup>, P. Cao<sup>1,2</sup>, L. Dong<sup>1,2</sup>

<sup>1</sup>University of Science and Technology of China, China; <sup>2</sup>the State Key Laboratory of Particle Detection and Electronics, China

### PS3-10

#### An FPGA-Based Readout Module for the DAQ Subsystem of the DSSC Detector at the European XFEL (\*) ) %

T. Gerlach, A. Kugel,

Heidelberg University, Germany

### PS3-11

#### Development of a High Resolution PXI Based Data Acquisition System for Electron Momentum Spectrometer (\*) ) ,

Y. Huang, S. Liu, J. Wang, X. Hu, C. Feng, Q. An

University of Science and Technology of China, China

**PS3-13**

**A High Speed High Resolution Digital Platform for the  $4\pi\beta\gamma$  Coincidence Counting System** \*) \* &

K. Zhang<sup>1,2</sup>, K. Song<sup>1,2</sup>, J. Yang<sup>1,2</sup>, P. Cao<sup>1,2</sup>, H. Wang<sup>1,2</sup>

<sup>1</sup>*University of Science and Technology of China, China; <sup>2</sup>the State Key Laboratory of Particle Detection and Electronics, China*

**PS3-14**

**The Readout Electronics of the Micromegas-Based Large Time Projection Chamber Prototype for the**

**International Linear Collider** \*) \*

D. Calvet, D. Attie, D. Besin, P. Colas, R. Joannes, A. Le Coguie, S. Lhenoret, I. Mandjavidze, M. Riallot, W. Wang, E. Zonca  
*CEA-IRFU, France*

**PS3-16**

**An FPGA Based GEMROC ASIC Readout System** \*) +\$

B. Mindur, W. Dabrowski, T. Fiutowski, P. Wiacek, A. Zielinska  
*AGH University of Science and Technology, Poland*

**PS3-17**

**Design and Test of a High-Speed Flash ADC Mezzanine Card for High-Resolution and Timing Performance for**

**Nuclear Structure Experiments** \*) +)

X. Egea Canet<sup>1,2</sup>, E. Sanchis<sup>2</sup>, V. Gonzalez<sup>2</sup>, A. Gadea<sup>1</sup>, J. M. Blasco<sup>2</sup>, D. Barrientos<sup>1,2</sup>, J. J. Valiente Dobon<sup>3</sup>, M. Tripon<sup>4</sup>, A. Boujrad<sup>4</sup>, C. Houarner<sup>4</sup>, M. Jastrzab<sup>5</sup>, G. de Angelis<sup>3</sup>, M. N. Erduran<sup>6</sup>, S. Erturk<sup>7</sup>, T. Huyuk<sup>1</sup>, G. Jaworski<sup>8,9</sup>, J. Nyberg<sup>10</sup>, M. Palacz<sup>9</sup>, G. de France<sup>4</sup>, A. di Nitto<sup>11</sup>, A. Pipidis<sup>3</sup>, R. Tarnowski<sup>9</sup>, R. Wadsworth<sup>12</sup>, A. Triassi<sup>3</sup>  
<sup>1</sup>*IFIC (Institut de fisica corpuscular), Spain; <sup>2</sup>UV (Universitat de Valncia), Spain; <sup>3</sup>INFN, Laboratori Nazionali di Legnaro, Italy; <sup>4</sup>Grand Accelerateur National d'Ions Lourds, France; <sup>5</sup>Niewodzanski Institute of nuclear physics, Polish Academy of Sciences, Poland; <sup>6</sup>Istanbul Sabahattin Zaim university Istanbul, Turkey; <sup>7</sup>Nigde Universitesi, Turkey; <sup>8</sup>Warzaw university of technology, Poland; <sup>9</sup>University of Warzaw, Poland; <sup>10</sup>Uppsala University, Sweden; <sup>11</sup>INFN, Sezione di Napoli, Italy; <sup>12</sup>University of York, United Kingdom*

**PS3-18**

**Design of an Optical Uplink with 10Gbit/s Link Between PCIe and MicroTCA** \*) , '

H. Kleines, P. Wstner, A. Ackens, M. Drochner, P. Kmmerling, S. van Waesen, M. Ramm  
*Forschungszentrum Jlich, Germany*

**PS3-19**

**Real-Time Data Acquisition for Long-Distance Reflective Ghost Imaging Experiment with Thermal Light** \*) , \*

F. Wen<sup>1,2</sup>, F. Li<sup>1,2</sup>, Q. Wang<sup>1,2</sup>, G. Jin<sup>1,2</sup>

<sup>1</sup>*University of Science and Technology of China, China; <sup>2</sup>State Key Laboratory of Particle Detection and Electronics, China*

**PS3-20**

**Upgrading the Backend of the Pipeline Readout System for Belle II** \*) - \$

S. Y. Suzuki, T. Higuchi, M. Nakao, R. Itoh, Y. Igashiki  
*KEK, Japan*

**PS3-21**

**Development of an AMC Module MMC** \*) - )

P. Kaemmerling, M. Drochner, H. Kleines, S. van Waesen, M. Ramm, A. Ackens  
*Forschungszentrum Juelich, Germany*

**PS3-22**

**Minimizing Dead Time of the Belle II Data Acquisition System with Pipelined Trigger Flow Control** \*) - ,

M. Nakao<sup>1</sup>, C. Lim<sup>2</sup>, M. Friedl<sup>3</sup>, T. Uchida<sup>1</sup>

<sup>1</sup>*KEK, High Energy Accelerator Research Organization, Japan; <sup>2</sup>Yonsei University, Korea; <sup>3</sup>HEPHY, Austrian Academy of Sciences, Austria*

**PS3-23**

**Development of New Data Acquisition System at Super-Kamiokande for Nearby Supernova Bursts** \*\* \$'

T. Tomura<sup>1</sup>, Y. Hayato<sup>1</sup>, M. Ikeda<sup>2</sup>, M. Nakahata<sup>1</sup>, S. Nakayama<sup>1</sup>, Y. Obayashi<sup>1</sup>, K. Okumura<sup>1</sup>, M. Shiozawa<sup>1</sup>, S. Y. Suzuki<sup>2</sup>, T. Uchida<sup>2</sup>, S. Yamada<sup>3</sup>, T. Yokozawa<sup>1</sup>

<sup>1</sup>*University of Tokyo, Japan; <sup>2</sup>High Energy Accelerator Research Organization (KEK), Japan; <sup>3</sup>Tohoku University, Japan*

**PS3-24**

**Development of a Clock Distribution System for Sub-Nanosecond Time Synchronization over Long Distances \*\*\*\$,**

Y. Yang, K. Hanson, T. Meures

*Interuniversity Institute for High Energies (IIHE), Brussels, Belgium*

**PS3-25**

**Development of the Data Acquisition System of a Large TPC for the ILC \*\*\*%&**

G. W. P. De Lentdecker<sup>1</sup>, E. Verhagen<sup>1</sup>, Y. Yang<sup>1</sup>, L. Jonsson<sup>2</sup>, B. Lundberg<sup>2</sup>, U. Mjornmark<sup>2</sup>, A. Oskarsson<sup>2</sup>, L. Osterman<sup>2</sup>, E. Stenlund<sup>2</sup>

<sup>1</sup>*Universite Libre de Bruxelles, Belgium;* <sup>2</sup>*Lund University, Sweden*

**PS3-26**

**Real-Time Performance of Commercial Intel-Based VME Controllers for the CODA Data Acquisition System \*\*\*%&**

B. J. Moffit,

*Jefferson Lab, United States*

**PS3-27**

**A Readout System Utilizing the APV25 ASIC for the Forward GEM Tracker in STAR \*\*\*%**

G. J. Visser<sup>1</sup>, J. T. Anderson<sup>2</sup>, B. Buck<sup>3</sup>, A. S. Kreps<sup>2</sup>, T. Ljubicic<sup>4</sup>

<sup>1</sup>*Indiana University, United States;* <sup>2</sup>*Argonne National Laboratory, United States;* <sup>3</sup>*Massachusetts Institute of Technology, United States;* <sup>4</sup>*Brookhaven National Laboratory, United States*

**PS3-28**

**A Comprehensive Zero-Copy Architecture for High Performance Distributed Data Acquisition over Advanced**

**Network Technologies for the CMS Experiment \*\*\*&**

A. Petrucci<sup>1</sup>, G. Bauer<sup>2</sup>, U. Behrens<sup>3</sup>, J. Branson<sup>4</sup>, S. Bukowiec<sup>1</sup>, O. Chaze<sup>1</sup>, S. Cittolin<sup>5</sup>, J. A. Coarasa Perez<sup>1</sup>, C. Deldicque<sup>1</sup>, M. Dobson<sup>1</sup>, A. Dupont<sup>1</sup>, S. Erhan<sup>6</sup>, D. Gigi<sup>1</sup>, F. Glege<sup>1</sup>, R. Gomez - Reino<sup>1</sup>, C. Hartl<sup>1</sup>, A. Holzner<sup>4</sup>, L. Masetti<sup>1</sup>, F. Meijers<sup>1</sup>, E. Meschi<sup>1</sup>, R. Mommsen<sup>7</sup>, C. Nunez-Barranco-Fernandez<sup>1</sup>, V. O'Dell<sup>7</sup>, L. Orsini<sup>1</sup>, C. Paus<sup>2</sup>, M. Pieri<sup>4</sup>, G. Polese<sup>1</sup>, A. Racz<sup>1</sup>, O. Raginel<sup>2</sup>, H. Sakulin<sup>1</sup>, M. Sani<sup>4</sup>, C. Schwick<sup>1</sup>, A. C. Cristian Spataru<sup>1</sup>, F. Stoeckli<sup>2</sup>, K. Sumorok<sup>2</sup>

<sup>1</sup>*CERN, Switzerland;* <sup>2</sup>*Massachusetts Institute of Technology, USA;* <sup>3</sup>*DESY, Germany;* <sup>4</sup>*University of California, San Diego, USA;* <sup>5</sup>*Eidgenossische Technische Hochschule, Switzerland;* <sup>6</sup>*University of California, Los Angeles, USA;*

<sup>7</sup>*FNAL, USA*

**PS3-29**

**A Novel Data Acquisition Scheme Based on a Low-Noise Front-End ASIC and a High-Speed ADC for CZT-**

**Based Small-Animal PET Imaging \*\*\*++**

W. Gao<sup>1</sup>, D. Gao<sup>1</sup>, B. Gan<sup>1</sup>, L. Wang<sup>1</sup>, Q. Zheng<sup>1</sup>, F. Xue<sup>1</sup>, T. Wei<sup>1</sup>, Y. Hu<sup>2</sup>

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**PS3-31**

**Communication Architecture of DAQ-Middleware \*\*\*++**

Y. Nagasaka<sup>1</sup>, H. Sendai<sup>2</sup>, E. Inoue<sup>2</sup>, T. Koutoku<sup>3</sup>, N. Ando<sup>3</sup>, S. Ajimura<sup>4</sup>, M. Wada<sup>5</sup>

<sup>1</sup>*Hiroshima Institute of Technology, Japan;* <sup>2</sup>*High Energy Accelerator Research Organization, Japan;* <sup>3</sup>*The National Institute of Advanced Industrial Science and Technology, Japan;* <sup>4</sup>*Osaka University, Japan;* <sup>5</sup>*Bee Beans Technologies Co. Ltd., Japan*

**PS3-32**

**Implementation of the Disruption Predictor APODIS in JET Real Time Network Using the MARTE Framework \*\*\*(\$**

J. M. Lopez<sup>1</sup>, J. Vega<sup>2</sup>, D. Alves<sup>3</sup>, S. Dormido-Canto<sup>4</sup>, A. Murari<sup>5</sup>, J. M. Ramirez<sup>4</sup>, R. Felton<sup>6</sup>, M. Ruiz<sup>1</sup>, G. D. Arcas<sup>1</sup>, and JET-EFDA Contributors<sup>7</sup>

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**PS3-33**

**A Versatile High Speed Data Acquisition Module with Four 10G Ethernet Links \*\*\*((**

I. Sheviakov, M. Zimmer

*Deutsches Elektronen-Synchrotron, Germany*

**PS3-35**

**Implementation of Intelligent Data Acquisition Systems for Fusion Experiment Using EPICS and FlexRIO Technology** \*\*\*(+

D. Sanz<sup>1</sup>, M. Ruiz<sup>1</sup>, R. Castro<sup>2</sup>, J. Vega<sup>2</sup>, J. M. Lopez<sup>1</sup>, E. Barrera<sup>1</sup>, N. Utzel<sup>3</sup>, P. Makijarvi<sup>3</sup>

<sup>1</sup>Universidad Politecnica de Madrid, Spain; <sup>2</sup>Asociacion EURATOM/CIEMAT, Spain; <sup>3</sup>ITER Organizorion, France

**PS3-36**

**DEAP-3600 Dark Matter Experiment Data Acquisition and Trigger System** \*\*\*))

A. J. Muir

TRIUMF, Canada

**PS3-37**

**A 16-Channel 15 ps TDC Implemented in a 65 nm FPGA** \*\*\*+\*

L. Zhao<sup>1,2</sup>, X. Hu<sup>1,2</sup>, S. Liu<sup>1,2</sup>, J. Wang<sup>1,2</sup>, Q. An<sup>1,2</sup>

<sup>1</sup>University of Science and Technology of China, China; <sup>2</sup>Department of Modern Physics, University of Science and Technology of China, China

**PS3-38**

**Development of High Resolution TDC Implemented in Radiation Tolerant FPGAs for Aerospace Application** \*\*\*+,

X. Qin<sup>1,2</sup>, C. Feng<sup>1,2</sup>, L. Zhao<sup>1,2</sup>, D. Zhang<sup>1,2</sup>, X. Hao<sup>1</sup>, S. Liu<sup>1,2</sup>, Q. An<sup>1,2</sup>

<sup>1</sup>University of Science and Technology of China, China; <sup>2</sup>State Key Laboratory of Technologies of Particle Detection & Electronics, China

**PS3-39**

**SEUs Tolerance in FPGAs Based Digital LLRF System for XFEL** \*\*\*+\*

M. K. Grecki,

DESY, Hamburg, Germany

**PS3-40**

**Maximum Likelihood Estimation and Non-Linear Least Squares Fitting with Levenberg-Marquardt Algorithm**

**Implementation in FPGA Devices for High Resolution Hodoscopy** \*\*\*+\*

J. M. Blasco, E. Sanchis, V. Gonzalez, J. D. Martin, X. Egea, D. Barrientos, D. Granero

Universidad de Valencia, Spain

**PS3-41**

**Multiple Register Synchronization with a High-Speed Serial Link Using the Aurora Protocol** \*\*\*, %

D. Barrientos<sup>1,2,3</sup>, V. Gonzalez<sup>3</sup>, M. Bellato<sup>2</sup>, A. Gadea<sup>1</sup>, D. Bazzacco<sup>2</sup>, J. M. Blasco<sup>3</sup>, D. Bortolato<sup>2</sup>, F. J. Egea<sup>1,3</sup>, R. Isocrate<sup>2</sup>, A. Pullia<sup>4</sup>, G. Rampazzo<sup>2</sup>, E. Sanchis<sup>3</sup>, A. Triossi<sup>2</sup>

<sup>1</sup>Instituto de Fisica Corpuscular (CSIC-UV), Spain; <sup>2</sup>Istituto di Fisica Nucleare (INFN), Sezione di Padova, Italy;

<sup>3</sup>Departamento Ingeniera Electronica, Universitat de Valencia, Spain; <sup>4</sup>Istituto di Fisica Nucleare (INFN), Sezione di Milano, Italy

**PS3-42**

**Graphical User Interface for Serial Protocols Through a USB Link** \*\*\* , \*

D. Barrientos<sup>1,2,3</sup>, V. Gonzalez<sup>3</sup>, M. Bellato<sup>2</sup>, A. Gadea<sup>1</sup>, D. Bazzacco<sup>2</sup>, J. M. Blasco<sup>3</sup>, D. Bortolato<sup>2</sup>, F. J. Egea<sup>1,3</sup>, R. Isocrate<sup>2</sup>, A. Pullia<sup>4</sup>, G. Rampazzo<sup>2</sup>, E. Sanchis<sup>3</sup>, A. Triossi<sup>2</sup>

<sup>1</sup>Instituto de Fisica Corpuscular (CSIC-UV), Spain; <sup>2</sup>Istituto di Fisica Nucleare (INFN), Sezione di Padova, Italy;

<sup>3</sup>Departamento Ingeniera Electronica, Universitat de Valencia, Spain; <sup>4</sup>Istituto di Fisica Nucleare (INFN), Sezione di Milano, Italy

**PS3-43**

**SEU Effects on Power Consumption in Xilinx FPGAs** \*\*\* - \$

A. Aloisio<sup>1,2</sup>, V. Bocci<sup>2</sup>, G. Chiodi<sup>2</sup>, R. Giordano<sup>1,2</sup>, V. Izzo<sup>2</sup>, L. Sterpone<sup>3</sup>, M. Violante<sup>3</sup>

<sup>1</sup>University of Naples 'Federico II' and INFN, Italy; <sup>2</sup>INFN, Italy; <sup>3</sup>Politecnico di Torino, Italy

**PS3-44**

**Online Software Time Calibration for a Continuous Air Shower Array** \*\*\* - )

S. Mastroianni<sup>1</sup>, M. Iacovacci<sup>2</sup>

<sup>1</sup>INFN Napoli, Italy; <sup>2</sup>Universita' Federico II and INFN sez. di Napoli, Italy

## FERT2 FPGA and Electronics Applied to Realtime Systems 2

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**FERT2-2**

**Hardware Timebase Calibration in the Multi-GSa/s LABRADOR-4 ASIC** \*\*\*+\$\$

G. S. Varner, M. Z. Andrew, Z. Cao, K. A. Nishimura, P. W. Gorham

Hawaii Univ., United States

## FERT2-3

**Time Interval Analyzer with FPGA-Based TDC for Free Space Quantum Key Distribution: Principle and**

**Validation with Prototype Setup**

Q. Shen<sup>1</sup>, S. Liao<sup>1</sup>, S. Liu<sup>1</sup>, J. Wang<sup>1</sup>, W. Liu<sup>2</sup>, C. Peng<sup>1</sup>, Q. An<sup>1</sup>

<sup>1</sup>*University of Science and Technology of China, China;* <sup>2</sup>*Ningbo University, China*

## FERT2-4

**128 Channels of Multi-GSa/s Waveform Sampling and Digitization in an 800 cm<sup>3</sup> Package**

M. Z. Andrew, C. N. Lim, K. A. Nishimura, L. J. Ridley, G. S. Varner

*University of Hawaii, United States*

## FERT2-5

**A Stepped-Up Tree Encoder for the 10-ps Wave Union TDC**

X. Hu<sup>1,2</sup>, L. Zhao<sup>1,2</sup>, S. Liu<sup>1,2</sup>, J. Wang<sup>1,2</sup>, Q. An<sup>1,2</sup>

<sup>1</sup>*University of Science and Technology of China, China;* <sup>2</sup>*Department of Modern Physics, University of Science and Technology of China, China*

## FERT2-6

**A Silicon Diode Based Detector for Radiation Measurement in High Altitude Natural Environment**

D. Pantel<sup>1</sup>, J.-R. Vaille<sup>1</sup>, F. Wrobel<sup>1</sup>, L. Dilillo<sup>2</sup>, J.-M. Galliere<sup>2</sup>, J.-L. Autran<sup>3</sup>, P. Cocquerez<sup>4</sup>, P. Chadoutaud<sup>4</sup>, F. Saigne<sup>1</sup>

<sup>1</sup>*Universite Montpellier 2, France;* <sup>2</sup>*LIRMM, France;* <sup>3</sup>*IM2NP, France;* <sup>4</sup>*CNES, France*

## PS4 Poster Session 4

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### PS4-1

**A General Self-Organization Tree-Based Energy-Balance Routing Protocol for Wireless Sensor Network**

Z. Han, J. Wu, J. Zhang, L. Liu, K. Tian

*University of Science and Technology of China, China, 230026*

### PS4-2

**Real Time Control System of Active Reflector of FAST** &

X.-C. Deng<sup>1,2</sup>, W.-Q. Wu<sup>1,2</sup>, M.-C. Luo<sup>1,2</sup>, H.-T. Shen<sup>3</sup>, L.-C. Zhu<sup>3</sup>, P.-Y. Tang<sup>1,2</sup>, J.-J. Liu<sup>1,2</sup>, F. Li<sup>1,2</sup>, G. Jin<sup>1,2</sup>, J. Wang<sup>1,2</sup>

<sup>1</sup>*Univ. of Sci. & Tech. of China, China;* <sup>2</sup>*State Key Laboratory of Technologies of Particle Detection and Electronics, China;* <sup>3</sup>*National Astronomical Observatories, China*

### PS4-3

**IPMI Test Software for MicroTCA Developments** \*

M. Drochner, P. Kaemmerling, H. Kleines, S. v.Waasen

*FZJ/ZEL, Germany*

### PS4-4

**The Research and Design of the Data Acquisition System and the Control System of KTX** -

J. An, K. Song, P. Cao, J. Yang

*the State Key Laboratory of Particle Detection and Electronics, China*

### PS4-6

**A Prototype GUI for the Multi-Channel Sensor Data Acquisition and Monitoring System of KTX** (

L. Dong<sup>1,2</sup>, K. Song<sup>1,2</sup>, J. Yang<sup>1,2</sup>, P. Cao<sup>1,2</sup>, D. Mao<sup>1,2</sup>, W. Lv<sup>1,2</sup>

<sup>1</sup>*University of Science and Technology of China, China;* <sup>2</sup>*State Key Laboratory of Particle Detection and Electronics, China*

### PS4-7

**Axisymmetric Magnetic Control in ITER**

L. Zabeo<sup>1</sup>, G. Ambrosino<sup>2</sup>, M. Cavinato<sup>3</sup>, Y. Gribov<sup>1</sup>, D. Humpreys<sup>4</sup>, J. A. Snipes<sup>1</sup>, M. Walker<sup>4</sup>, A. Kavin<sup>5</sup>, V. Lukash<sup>6</sup>, G. Vayakis<sup>1</sup>

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### PS4-8

**Present Status of the ITER Real-Time Plasma Control System Development** )

A. Winter, P. Makijarvi, S. Simrock, J. Snipes, A. Wallander, L. Zabeo

*ITER Organization, France*

**PS4-9****Experiences with the MTCA.4 Solution for the EuXFEL Clock and Control System** \*\*\*+\*%

E. Motuk, M. Postranecky, M. Warren, M. Wing  
University College London, United Kingdom

**PS4-10****New strategy for the control of low frequency large band mechanical suspensions and inertial platforms** \*\*\*+\*+

F. Barone<sup>1,2</sup>, F. Acerneese<sup>1,2</sup>, R. De Rosa<sup>3,2</sup>, G. Giordano<sup>1</sup>, R. Romano<sup>1,2</sup>

<sup>1</sup>Università di Salerno, Italy; <sup>2</sup>Istituto Nazionale di Fisica Nucleare, Italy; <sup>3</sup>Università di Napoli Federico II, Italy

**PS4-11****Superconducting Cavities Automatic Loaded Quality Factor Control at FLASH** \*\*\*++%

W. Cichalewski<sup>1</sup>, J. Branlard<sup>2</sup>, H. Schlarb<sup>2</sup>, N. Walker<sup>2</sup>, J. Carwardine<sup>3</sup>

<sup>1</sup>Technical University of Lodz, Poland; <sup>2</sup>Deutsches Elektronen Synchrotron, Germany; <sup>3</sup>Argonne National Laboratory, USA

**PS4-12****Timing and Triggering System for the European XFEL Project - a Double Sized AMC Board** \*\*\*+++

A. Hidvegi<sup>1</sup>, P. Gessler<sup>2</sup>, H. Kay<sup>3</sup>, K. Rehlich<sup>3</sup>, C. Bohm<sup>1</sup>

<sup>1</sup>Stockholm University, Sweden; <sup>2</sup>European X-Ray Free Electron Laser Facility GmbH, Germany; <sup>3</sup>Deutsches Elektronen-Synchrotron (DESY), Germany

**PS4-13****Secure and Reliable Remote Access for the European XFEL Control System** \*\*\*+, \$

C. C. W. Robson<sup>1</sup>, C. Bohm<sup>1</sup>, K. Rehlich<sup>2</sup>, R. Kammering<sup>2</sup>

<sup>1</sup>Stockholms Universitet, Sweden; <sup>2</sup>Deutsches Elektronen-Synchrotron, Germany

**PS4-17****High-Performance Scalable Information Service for the ATLAS Experiment.** \*\*\*+, (

S. Kolos

University of California Irvine, USA

**PS4-18****Recent Developments in Control Software for Optical Synchronization Applications at DESY** \*\*\*+, -

P. Predki, T. Kozak, A. Napieralski

Technical University of Lodz, Poland

**PS4-19****The New Generation of the LHC Accelerator Radiation Monitoring System** \*\*\*+-+

A. Masi, M. Brugger, M. Donze` , G. Spiezio, P. Peronnard

CERN, Switzerland

**PS4-22****Development of an ATCA Based Data Acquisition System for High Speed, Direct Detection X-Ray Pixel**

**Sensors** \*\*\*, \$(

J. Joseph<sup>1</sup>, D. Contarato<sup>1</sup>, P. Denes<sup>1</sup>, D. Doering<sup>1</sup>, P. McVittie<sup>1</sup>, J. Weizeorick<sup>2</sup>

<sup>1</sup>Lawrence Berkeley National Laboratory, United States; <sup>2</sup>Argonne National Laboratory, United States

**PS4-23****Data Acquisition System Based on Time-Interleaved Analog-to-Digital Conversion for Time-of-Flight Mass**

**Spectrometer** \*\*, \$-

X. Hu, L. Zhao, W. Zheng, S. Liu, Q. An

University of Science and Technology of China, China

## FERT3 FPGA and Electronics Applied to Realtime Systems 3

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**FERT3-1****Real-Time Clustering for Pixel Detectors: the DCE3 ASIC for the PXD Detector in the Belle II Experiment**

@KEK \*\*\*, %

A. Wassatsch, R. Richter

Max-Planck-Institut fuer Physik, Germany

**FERT3-2****Quantization Analysis of the Infrared Interferometer of the TJ-II for Its Optimized FPGA-Based Implementation** \*\*, 8%

L. Esteban<sup>1</sup>, J. A. Lopez<sup>2</sup>, E. Sedano<sup>2</sup>, M. Sanchez<sup>1</sup>

<sup>1</sup>Centro de Investigaciones Energeticas Medioambientales y Tecnologicas, Spain; <sup>2</sup>Universidad Politecnica de Madrid, Spain