

2013 IEEE 14th Workshop on Control and Modeling for Power Electronics

(COMPEL 2013)

**Salt Lake City, Utah, USA
23-26 June 2013**



IEEE Catalog Number: CFP13COM-POD
ISBN: 978-1-4673-4915-4

TABLE OF CONTENTS

CLOSED-LOOP di/dt & du/dt CONTROL AND DEAD TIME MINIMIZATION OF IGBTs IN BRIDGE LEG CONFIGURATION.....	1
<i>Yanick Lobsiger, Johann W. Kolar</i>	
DC TECHNOLOGY FOR STABLE AND RELIABLE GRIDS	8
<i>Mohamed Y. Haj-Maharsi</i>	
STATE-SPACE SWITCHING MODEL OF MODULAR MULTILEVEL CONVERTERS.....	15
<i>Jun Wang, Ewan Farr, Rolando Burgos, Dushan Boroyevich, Ralph Feldman, Alan Watson, Jon Clare, Pat Wheeler</i>	
IMPEDANCE-BASED STABILITY ANALYSIS OF VSC-BASED HVDC SYSTEMS.....	25
<i>Hanchao Liu, Jian Sun</i>	
HVDC TRANSMISSION SYSTEM ARCHITECTURES AND CONTROL – A REVIEW	33
<i>Shahil Shah, Rohail Hassan, Jian Sun</i>	
IMPLEMENTATION OF DITHERING DIGITAL RIPPLE CORRELATION CONTROL FOR PV MAXIMUM POWER POINT TRACKING	41
<i>Christopher Barth, Robert C. N. Pilawa-Podgurski</i>	
MULTILEVEL POWER-POINT-TRACKING FOR VARIABLE-CONVERSION-RATIO PHOTOVOLTAIC LADDER CONVERTERS	48
<i>Christopher Schaefer, Jason T. Stauth</i>	
PHOTOVOLTAIC HOT SPOT ANALYSIS FOR CELLS WITH VARIOUS REVERSE-BIAS CHARACTERISTICS THROUGH ELECTRICAL AND THERMAL SIMULATION	55
<i>Katherine A. Kim, Philip T. Krein</i>	
PV FAULTS: OVERVIEW, MODELING, PREVENTION AND DETECTION TECHNIQUES	63
<i>Mohammed Khorshed Alam, Faisal H. Khan</i>	
MODELING AND SIMULATION OF LARGE SCALE POWER SYSTEMS IN MORE ELECTRIC AIRCRAFT	70
<i>Y. Ji, M. R. Kuhn</i>	
VALIDATION OF FREQUENCY- AND TIME-DOMAIN FIDELITY OF AN ULTRA-LOW LATENCY HARDWARE-IN-THE-LOOP (HIL) EMULATOR.....	76
<i>Elaina Chai, Ivan Celanovic, Jason Poon</i>	
REAL-TIME HARDWARE-IN-THE-LOOP SIMULATION OF CONVERTIBLE STATIC TRANSMISSION CONTROLLER FOR TRANSMISSION GRID MANAGEMENT	81
<i>Nima Yousefpoor, Ali Azidehak, Subhashish Bhattacharya, Babak Parkhideh, Ivan Celanovic, Adrien Genic</i>	
THREE-PHASE IMPEDANCE MEASUREMENT FOR SYSTEM STABILITY ANALYSIS.....	89
<i>Mauricio Cespedes, Jian Sun</i>	
LYAPUNOV-BASED DIGITAL CONTROL OF GRID-CONNECTED INVERTER WITH AN LCL FILTER	95
<i>Toshiji Kato, Kaoru Inoue, Masahiko Ueda</i>	
NARROWBAND DAMPING OF GRID-CONNECTED INVERTERS FOR HARMONIC RESONANCE MITIGATION.....	100
<i>Mauricio Cespedes, Jian Sun</i>	
ONLINE GRID IMPEDANCE MEASUREMENT USING DISCRETE-INTERVAL BINARY SEQUENCE INJECTION	107
<i>Tomi Roinila, Matti Vilkkko, Jian Sun</i>	
A MODULAR BIDIRECTIONAL DC POWER FLOW CONTROLLER WITH FAULT BLOCKING CAPABILITY FOR DC NETWORKS	115
<i>Gregory J. Kish, Peter W. Lehn</i>	
MAXIMUM POWER POINT TRACKING OF STIRLING GENERATOR AND OCEAN WAVE ENERGY CONVERSION SYSTEMS USING A TWO-STAGE POWER CONVERTER.....	122
<i>Qian Li, Faisal H. Khan, Abusaleh M. Imtiaz</i>	
SWISS RECTIFIER OUTPUT VOLTAGE CONTROL WITH INNER LOOP POWER FLOW PROGRAMMING (PFP).....	128
<i>P. Cortes, M. F. Vancu, J. W. Kolar</i>	
IMPROVING THE LIGHT LOAD EFFICIENCY AND THD OF PFC CONVERTERS USING A LINE CYCLE SKIPPING METHOD	136
<i>Christopher Fiorentino, Zhiyuan Hu, Yan-Fei Liu</i>	

FEEDFORWARD COMPENSATION OF RESONANT CONVERTERS WITH HEAVY RIPPLE IN THE DC BUS FOR LED LAMP DRIVER APPLICATIONS	143
<i>Christian Branäs, Francisco J. Azcondo, Rosario Casanueva</i>	
ON/OFF CONTROL OF A MODULAR DC-DC CONVERTER BASED ON ACTIVE-CLAMP LLC MODULES	149
<i>Hien Nguyen, Dragan Maksimovic, Regan Zane</i>	
MODELING AND CONTROL OF A HIGH BOOST RATIO PV MODULE DC-DC CONVERTER WITH DOUBLE GRID-LINE RIPPLE REJECTION	155
<i>Bin Gu, Jason Dominic, Jih-Sheng Lai</i>	
RESONANT SWITCHED-CAPACITOR CONVERTERS FOR CHIP-SCALE POWER DELIVERY: MODELING AND DESIGN	159
<i>Kapil Kesarwani, Rahul Sangwan, Jason T. Stauth</i>	
ANALYSIS OF SWITCHED-CAPACITOR DC-DC CONVERTERS IN SOFT-CHARGING OPERATION	166
<i>Yutian Lei, Robert C. N. Pilawa-Podgurski</i>	
FEEDBACK CONTROL OF PHASE SHIFT MODULATED HALF BRIDGE CIRCUITS FOR ZERO VOLTAGE SWITCHING ASSISTANCE	173
<i>Daniel Seltzer, Regan Zane</i>	
CONTROL STRATEGY FOR MODULAR DUAL ACTIVE BRIDGE INPUT SERIES OUTPUT PARALLEL	180
<i>P. Zumel, L. Ortega, A. Lazaro, C. Fernandez, A. Barrado</i>	
FAST SWITCHING RECONFIGURABLE PHOTOVOLTAIC MODULES INTEGRATED WITHIN DC-DC CONVERTERS	187
<i>Jen-Hung Huang, Ye Zhao, Brad Lehman, Dzong Nguyen</i>	
DIGITAL IMPLEMENTATION OF LOAD SHARING METHOD FOR INTERLEAVED LLC CONVERTERS	194
<i>Zhiyuan Hu, Yajie Qiu, Yan-Fei Liu</i>	
COHERENT FEEDFORWARD IMPEDANCE CORRECTION AND FEEDBACK POWER REGULATION IN A PLASMA PROCESSING RF POWER DELIVERY SYSTEM	201
<i>David J. Coumou</i>	
A VIRTUAL LABORATORY FOR POWER ELECTRONICS AND DSP BASED MOTION CONTROL	205
<i>Xiong Li, U. R. Prasanna, Akin Bilal, Kaushik Rajashekara</i>	
AN INTERFACE BOARD FOR DEVELOPING CONTROL LOOPS IN POWER ELECTRONICS BASED ON MICROCONTROLLERS AND DSPS CORES –ARDUINO /CHIPKIT /DSPIC /DSP /TI PICCOLO	213
<i>Riccardo Pittini, Zhe Zhang, Michael A. E. Andersen</i>	
SIMPLE AND FAST MAXIMUM POWER POINT TRACKING FOR GRID CONNECTED PHOTOVOLTAIC SYSTEMS	220
<i>Abdelhalim Sandali, Oukhoya Tarik, Ahmed Cheriti</i>	
TRANSFER FUNCTION MAPPING FOR A GRID CONNECTED PV SYSTEM USING REVERSE SYNTHESIS TECHNIQUE	228
<i>Mohammed Khorshed Alam, Faisal H. Khan</i>	
ENERGY MANAGEMENT FOR SOLAR BATTERY CHARGING STATION	233
<i>Su Sheng, Chung-Ti Hsu, Peng Li, Brad Lehman</i>	
A SIMPLE FORMULA FOR ESTIMATING THE OPTIMUM TILT ANGLES OF PHOTOVOLTAIC PANELS	241
<i>Stephanie White Quinn, Brad Lehman</i>	
BOOST CONVERTER WITH COMBINED CONTROL LOOP FOR A STAND-ALONE PHOTOVOLTAIC BATTERY CHARGE SYSTEM	249
<i>Maria C. Mira, Arnold Knott, Ole C. Thomsen, Michael A. E. Andersen</i>	
A BRANCH AND BOUND ALGORITHM FOR HIGH-GRANULARITY PV SIMULATIONS WITH POWER LIMITED SUBMICS	257
<i>Carlos Olalla, Daniel Clement, Beom Seok Choi, Dragan Maksimovic</i>	
VOLTAGE STABILITY ANALYSIS FOR AIRCRAFT VARIABLE FREQUENCY GENERATION SYSTEM LOADED WITH PWM RECTIFIER	263
<i>Xiancheng Zheng, Weiguo Liu, Xiaobin Zhang</i>	
MODELING AND ANALYSIS OF DC-LINK HARMONIC INSTABILITY IN LCC HVDC SYSTEMS	268
<i>Hanchao Liu, Jian Sun</i>	
DC TERMINAL IMPEDANCE MODELING OF LCC-BASED HVDC CONVERTERS	277
<i>Hanchao Liu, Jian Sun</i>	

PRE-ENERGIZED COMPACT AUXILIARY CIRCUIT TO BUFFER LOADS FROM FAST TRANSIENTS WITH THE GOAL OF MANAGING LOAD-INFORMED POWER	285
<i>Zhenyu Shan, Philip T. Krein, Chi K. Tse, Siew-Chong Tan</i>	
DESIGN AND CONTROL OF A BIDIRECTIONAL TRIANGULAR MODULAR MULTILEVEL DC-DC CONVERTER	289
<i>Kia Filsoof, Peter W. Lehn</i>	
DESIGN AND IMPLEMENTATION OF THREE-PHASE GRID-CONNECTED TWO-STAGE MODULE INTEGRATED CONVERTER	296
<i>Frank Chen, Qian Zhang, Ahmadreza Amirahmadi, Issa Batarseh</i>	
INPUT CURRENT RIPPLE REDUCTION THROUGH INTERLEAVING IN SINGLE-SUPPLY MULTIPLE-OUTPUT DC-DC CONVERTERS	304
<i>Marcel Schuck, Robert C. N. Pilawa-Podgurski</i>	
SIMPLE DESIGN STRATEGY FOR MODULAR INPUT-SERIES OUTPUT-SERIES CONVERTERS	309
<i>C. Fernandez, P. Zumel, A. Lazaro, M. Sanz, A. Barrado</i>	
COMPREHENSIVE ANALYSIS AND COMPARATIVE EVALUATION OF THE ISOLATED TRUE BRIDGELESS CUK SINGLE-PHASE PFC RECTIFIER SYSTEM	318
<i>Dominik Bortis, Lukas Fassler, Johann W. Kolar</i>	
LOW THDI FRONT-END STAGE UNDER NON-SINUSOIDAL VOLTAGE	327
<i>V. M. Lopez, F. J. Azcondo</i>	
ANALYSIS OF HIGH EFFICIENCY DC/DC CONVERTER PROCESSING PARTIAL INPUT/OUTPUT POWER	334
<i>Junjian Zhao, Kenton Yeates, Yehui Han</i>	
COMPARATIVE HARMONIC ANALYSIS OF HYBRID 2D-3D SVM AND CONVENTIONAL 2D SVM	342
<i>Saher Albatran, Yong Fu, Ahmad Albanna</i>	
GENERALIZATION OF PARALLEL ANALYSIS FOR A POWER ELECTRONIC SYSTEM BY CIRCUIT PARTITIONING	349
<i>Toshiji Kato, Kaoru Inoue, Yukiko Kotani, Takumi Ogawa</i>	
SIMPLIFIED ADAPTIVE BACKSTEPPING CONTROL OF BUCK DC:DC CONVERTER WITH UNKNOWN LOAD	356
<i>Michael L. McIntyre, Michael Schoen, Joseph Latham</i>	
NUMERICAL AVERAGE VALUE MODELING OF SECOND ORDER FLYBACK CONVERTER IN BOTH OPERATIONAL MODES	363
<i>S. Amini Akbarabadi, M. Sucu, H. Atighechi, J. Jatskevich</i>	
MODELING OF A HIGH RESOLUTION DCM TIMES FEEDBACK LOOP FOR SENSORLESS BOOST PFC STAGES	369
<i>V. M. Lopez-Martin, F. J. Azcondo</i>	
DISTORTING IMPROVING METHOD FOR THREE-PHASES FULL BRIDGE CONVERTER BASED SWITCHED RELUCTANCE MOTOR DRIVE SYSTEM	375
<i>K. Tungpimolrut, S. Kachapornkul, P. Jitkreeyarn, P. Somsiri, N. Chayopitak</i>	
OPERATING BEHAVIOR AND DESIGN OF THE HALF-CYCLE DISCONTINUOUS-CONDUCTION-MODE SERIES-RESONANT- CONVERTER WITH SMALL DC LINK CAPACITORS	379
<i>Daniel Rothmund, Jonas E. Huber, Johann W. Kolar</i>	
A NEW RESONANT IPT-WIRELESS EV CHARGING SYSTEM WITH SINGLE-ENDED QUASI-RESONANT INVERTER FOR HOME USE	388
<i>Hideki Omori, Mutsuo Nakaoka, Yuichi Iga</i>	
NORMALIZED GEOMETRICAL ANALYSIS: UNIFIED THEORY AND DERIVATION OF NATURAL TRAJECTORIES FOR BASIC DC-DC TOPOLOGIES	395
<i>Juan M. Galvez, Martin Ordóñez, Matias Anun</i>	
MULTI-OBJECTIVE LAYOUT OPTIMIZATION FOR MULTI-CHIP POWER MODULES CONSIDERING ELECTRICAL PARASITICS AND THERMAL PERFORMANCE	400
<i>Brett W. Shook, Andalib Nizam, Zihao Gong, A. Mathew Francis, H. Alan Mantoosh</i>	
DC-DC CONVERTER CURRENT MEASUREMENT BY USING FET ON-STATE RESISTANCE FOR LOW-COST MAXIMUM POWER POINT TRACKING	404
<i>Noriyuki Kimura</i>	
TOPOLOGY ZERO: A GENERALIZED AVERAGING EQUIVALENT CIRCUIT MODEL FOR BASIC DC-DC CONVERTERS	409
<i>Matias Anun, Martin Ordóñez, Federico Luchino</i>	

RFPA SUPPLY MODULATOR USING WIDE-BANDWIDTH LINEAR AMPLIFIER WITH A GAN HEMT OUTPUT STAGE	416
<i>Dongxue Li, Miguel Rodriguez, Andrew Zai, David Sardin, Dragan Maksimovic, Zoya Popovic</i>	
DESIGN OF RESISTIVE-INPUT CLASS E RESONANT RECTIFIERS FOR VARIABLE-POWER OPERATION	422
<i>Juan A. Santiago-González, Khurram K. Afridi, David J. Perreault</i>	
CONTACTLESS POWER DELIVERY FOR MOBILE DEVICE CHARGING APPLICATIONS	428
<i>Xu Chen, Yung C. Liang</i>	
A NEW CURRENT CONTROL FOR EDDY CURRENT LOSS REDUCTION OF SWITCHED RELUCTANCE MOTORS	432
<i>Hiroki Ishikawa, Masahide Uraji, Haruo Naitoh</i>	
PRACTICAL CHARACTERIZATION OF EMI FILTERS REPLACING CISPR 17 APPROXIMATE WORST CASE MEASUREMENTS	438
<i>I. Kovacevic, F. Krismer, S. Schroth, J. W. Kolar</i>	
OPTIMIZATION OF ROTARY TRANSFORMER FOR HIGH-SPEED APPLICATIONS	448
<i>Dominik Bortis, Ivana Kovacevic, Lukas Fassler, Johann W. Kolar</i>	
PERFORMANCE AND EFFICIENCY ENHANCEMENT OF INDUCTION MACHINE DRIVES: DUALITY-BASED APPROACH TO VSI AND CSI CONTROL	454
<i>Srikanthan Sridharan, Philip T. Krein</i>	
###DESIGN METHODOLOGY FOR A HIGH-Q SELF-RESONANT COIL FOR MEDICAL AND WIRELESS-POWER APPLICATIONS	462
<i>Charles R. Sullivan, Lotfi Beghou</i>	
MIXED MOSFET-IGBT BRIDGE FOR HIGH-EFFICIENT MEDIUM-FREQUENCY DUAL-ACTIVE-BRIDGE CONVERTER IN SOLID STATE TRANSFORMERS	470
<i>G. Ortiz, Ch. Gammeter, J. W. Kolar, O. Apeldoorn</i>	
COMPARATIVE EVALUATION OF SIC AND SI PV INVERTER SYSTEMS BASED ON POWER DENSITY AND EFFICIENCY AS INDICATORS OF INITIAL COST AND OPERATING REVENUE	478
<i>Ralph M. Burkart, Johann W. Kolar</i>	
MODELING AND OPTIMIZATION OF AN INDUCTIVE MAGNETIC HARVESTER CONSIDERING NONLINEAR EFFECTS	484
<i>Qian Sun, Sumeet Patil, Nian-Xiang Sun, Brad Lehman</i>	
DESIGN AND SIMULATION OF CONTOUR MODE MEMS RESONATOR ON SI FOR POWER CONVERTERS	490
<i>Abusaleh M. Imtiaz, Faisal H. Khan</i>	
HIGH SPEED THERMAL COUPLING MEASUREMENTS FOR MULTICHIP ELECTRO-THERMAL MODEL VALIDATION	497
<i>John Reichl, Jose M. Ortiz-Rodriguez, Allen Hefner, Jih-Sheng Lai</i>	
COMPARISON OF REVERSE RECOVERY BEHAVIOR OF SILICON AND WIDE BANDGAP DIODES IN HIGH FREQUENCY POWER CONVERTERS	503
<i>Daniel Costinett, Regan Zane, Alberto Rodriguez, Aitor Vazquez</i>	
CHARACTERIZATION AND MODELING OF SIC JUNCTION BARRIER SCHOTTKY DIODE FOR CIRCUIT SIMULATION	511
<i>Shamim Ahmed, H. Alan Mantooh, Mihir Mudholkar</i>	
SRH BASED TRAP MODEL FOR GAN MMIC POWER SWITCHES	516
<i>Aaron Pereira, Sayed Albahrani, Anthony Parker, Graham Town, Michael Heimlich, Neil Weste</i>	
MODELING OF INJECTION ENHANCED IGBT FOR ACCURATE OF SWITCHING PERFORMANCE	520
<i>Takao Yamamoto, Hisato Kato, Masataka Miyake, Uwe Feldmann, Hans Jurgen Mattausch, Mitiko Miura-Mattausch</i>	
COMPACT SUPER-JUNCTION MOSFET MODEL CONSIDERING THE SPECIFIC POTENTIAL DISTRIBUTION DUE TO 2-FOLD RESISTANCE	525
<i>M. Kitamura, T. Umeda, A. Saito, M. Miyake, T. Inoue, F. Ueno, H. Kikuchi-hara, H. J. Mattausch, M. Miura-Mattausch</i>	
A NEW POWER MOSFET MODEL AND AN EASY TO USE CHARACTERIZATION TOOL USING DEVICE DATASHEET	530
<i>Fang Wang, Sameer Kher, Torsten Fichtner, Joachim Aurich</i>	
AN IMPROVED MODEL OF HIGH-VOLTAGE POWER LDMOSFET AND ITS USAGE IN MULTI-OBJECTIVE OPTIMIZATION OF RADIO-FREQUENCY AMPLIFIERS	535
<i>Josef Dobs, Vaclav Panko, Stanislav Banas, David Cerny</i>	
DESIGN OF TOROIDAL INDUCTORS WITH MULTIPLE PARALLEL FOIL WINDINGS	540
<i>Jizheng Qiu, Alex J. Hanson, Charles R. Sullivan</i>	

DESIGN AND ANALYSIS OF RIPPLE-BASED CONTROLLERS FOR BUCK CONVERTERS BASED ON DISCRETE MODELING AND FLOQUET THEORY	546
<i>Jorge Cortes, Vladimir Svikovic, Pedro Alou, Jesus A. Oliver, Jose A. Cobos</i>	
LARGE-SIGNAL AVERAGE MODELING OF HVDC SYSTEM TRANSIENTS USING PARAMETRIC AND ANALYTICAL APPROACHES	555
<i>H. Atighechi, S. Amini Akbarabadi, J. Jatskevich, S. Chiniforoosh</i>	
Author Index	