2015 21st IEEE International Symposium on Asynchronous Circuits and Systems

(ASYNC 2015)

Mountain View, California, USA
4 – 6 May 2015

IEEE Catalog Number: CFP15012-POD
ISBN: 978-1-4799-8717-7

2015 21st IEEE International Symposium on Asynchronous Circuits and Systems

ASYNC 2015

Table of Contents

Session 1: Crossing Clock Boundaries

Session 2: Circuit Design and Case Studies

Session 3: Demos

Session 4: Physical Design and Optimization

Session 5: New Perspectives

Session 6: Cryptoprocessors and NOCs

Session 7: Fresh Ideas

Session 8: Merge, Mutual Exclusion, and Arbitration

Session 9: Emerging Technologies