2015 IEEE 18th International Symposium on Design and Diagnostics of Electronic Circuits & Systems

(DDECS 2015)

Belgrade, Serbia
22 – 24 April 2015

IEEE Catalog Number: CFP15DDE-POD
ISBN: 978-1-4799-6781-0

2015 IEEE 18th International Symposium on Design and Diagnostics of Electronic Circuits & Systems

DDECS 2015

Table of Contents

Session 2B: Analog Circuits

Poster Session I

Session 3A: Modeling and Simulation

Session 3B: Student Session

Session 4A: Formal Verification

Session 4B: Design Enhancement

Session 5A: Wear-Out & Intermittent Faults

Session 5B: Measurement & Analysis

Poster Session II