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Technical Papers

CSA 1	NOC Design
Date / Time	Monday, October 19, 2015/10:15 – 11:35
Session Chair	Paul Gratz, *Texas A&M University*
Room	*405, Kimmel Center*

TVS 1	Verification
Date / Time	Monday, October 19, 2015/10:15 – 11:35
Session Chair	Masahiro Fujita, *University of Tokyo*
Room	*406, Kimmel Center*

CSA 2	Energy and Performance Optimization
Date / Time	Monday, October 19, 2015/11:40 – 13:00
Session Chair	Mingoo Seok, *Columbia University*
Room	*405, Kimmel Center*

PA 1	Optimizing Cache Access
Date / Time	Monday, October 19, 2015/14:00 – 15:20
Chair	Gang Quan, *Florida International University*
Room	*406, Kimmel Center*

LCD 1	Application Oriented Design
Date / Time	Monday, October 19, 2015/15:30 – 16:30
Session Chair	Jie Gu, *Northwestern University*
Room	*KC 405*

SS 1	Data Mining for Computer Design
Date / Time	Monday, October 19, 2015/15:30 – 16:30
Session Chair	Antonio Miele, *Politecnico di Milano, Italy*
Room	*KC 406*

CSA 4	Heterogeneous Computing Systems
Date / Time	Tuesday, October 20, 2015/09:30 – 10:30
Session Chair	Houman Homayoun, *George Mason University*
Room	*KC 405*

SS 2	Cyber-physical Integration and Design Automation for Microfluidic Biochips
Date / Time	Tuesday, October 20, 2015/09:30 – 10:30
Session Chair	Moderator: Jiang Hu, *Texas A&M University*
Room	*KC 406*

EDA 2	Logic and Layout Synthesis
Date / Time	Tuesday, October 20, 2015/10:50 – 12:30
Session Chair	Kyle Rupnow, *Advanced Digital Sciences Center, Singapore*
Room	*KC 405*

TVS 2	Security
Date / Time	Tuesday, October 20, 2015/10:50 – 12:30
Session Chair	Simha Sethumadhavan, *Columbia University*
Room	*KC 406*

PA 2	Optimization for Power and Speed
Date / Time	Tuesday, October 20, 2015/13:50 – 15:30
Session Chair	Nikos Hardavellas, *Northwestern University*
Room	*KC 405*

LCD 2	Resistive Memories
Date / Time	Tuesday, October 20, 2015/13:50 – 15:30
Session Chair	Mihalis Maniatakos, *New York University Abu Dhabi*
Room	*406, Kimmel Center*

PS 1	Poster Session
Date / Time	Tuesday, October 20, 2015/15:30 – 16:30
Room	*Eisner & Lubin Auditorium*

LCD 3	Design of Special Function Circuits
Date / Time	Wednesday, October 21, 2015/08:30 – 10:10
Session Chair	Bo Yuan, *City College NY*
Room	*KC 405*

PA 3	Architecting Processors Using New Circuit Technologies and Topologies
Date / Time	Wednesday, October 21, 2015/08:30 – 10:10
Session Chair	Shinobu Miwa, *The University of Electro-Communications*
Room	*KC 406*

CSA 5	Managing Multi-Core Systems
Date / Time	Wednesday, October 21, 2015/10:30 – 12:10
Session Chair	Ann Gordon-Ross, *University of Florida*
Room	*KC 405*

TVS 3	Test Optimization
Date / Time	Wednesday, October 21, 2015/10:30 – 12:10
Session Chair	Adit Singh, *Auburn University*
Room	*KC 406*

CSA 6	Performance Monitoring and Characterization
Date / Time	Wednesday, October 21, 2015/13:30 – 15:10
Session Chair	Sisu Xi, *Two Sigma Securities*
Room	*KC 405*

EDA 3	Improving Computational Efficiency of Circuit Analysis
Date / Time	Wednesday, October 21, 2015/13:30 – 15:10
Session Chair	Tsung-Yi Ho, *National Cheng Kung University, Taiwan*
Room	*KC 406*

CSA 7	Application-Specific Computation
Date / Time	Wednesday, October 21, 2015/15:30 – 16:30
Chair	Jiang Li, *Shanghai Jiao Tong University*
Room	*KC 405*

SS 3	Reliable and Secure Mobile Cognition
Date / Time	Wednesday, October 21, 2015/15:30 – 16:30
Session Chair	Karthik Swaminathan, *IBM T. J. Watson*
Room	*KC 406*

- **Resilient Mobile Cognition: Algorithms,Innovations, and Architectures 728**
  R. Viguier, C-C. Lin, K. Swaminathan, A. Vega, A. Buyuktosunoglu, S. Pankanti, P. Bose, H. Akbarpour, F. Bunyak, K. Palaniappan, G. Seetharaman
- **A Testing Platform for On-drone Computation 732**
  Wang Zhou, Dhruv Nair, Oki Gunawan, Theodore van Kessel and Hendrik F. Hamann
- **Resilient, UAV-Embedded Real-Time Computing 736**
  Augusto Vega, Chung-Ching Lin, Karthik Swaminathan, Alper Buyuktosunoglu, Sharathchandra Pankanti and Pradip Bose