

2016 IEEE Dallas Circuits and Systems Conference (DCAS 2016)

**Arlington, Texas, USA
10 October 2016**



**IEEE Catalog Number: CFP16505-POD
ISBN: 978-1-5090-2758-3**

**Copyright © 2016 by the Institute of Electrical and Electronics Engineers, Inc
All Rights Reserved**

Copyright and Reprint Permissions: Abstracting is permitted with credit to the source. Libraries are permitted to photocopy beyond the limit of U.S. copyright law for private use of patrons those articles in this volume that carry a code at the bottom of the first page, provided the per-copy fee indicated in the code is paid through Copyright Clearance Center, 222 Rosewood Drive, Danvers, MA 01923.

For other copying, reprint or republication permission, write to IEEE Copyrights Manager, IEEE Service Center, 445 Hoes Lane, Piscataway, NJ 08854. All rights reserved.

******This publication is a representation of what appears in the IEEE Digital Libraries. Some format issues inherent in the e-media version may also appear in this print version.***

IEEE Catalog Number:	CFP16505-POD
ISBN (Print-On-Demand):	978-1-5090-2758-3
ISBN (Online):	978-1-5090-2757-6

Additional Copies of This Publication Are Available From:

Curran Associates, Inc
57 Morehouse Lane
Red Hook, NY 12571 USA
Phone: (845) 758-0400
Fax: (845) 758-2633
E-mail: curran@proceedings.com
Web: www.proceedings.com

CURRAN ASSOCIATES INC.
proceedings
.com

DCAS2016 - Table Of Contents

CMT		Paper Title	Affiliation
Analog			
p6	A1.pdf	Speaker load diagnostics for switching automotive audio amplifiers	Texas Instruments, Dallas
p23	A2.pdf	Digital Adjustment of DC Motor Drive Circuit Parameters	Texas Instruments, Dallas
p25	A3.pdf	Design Considerations of CMOS Active Inductor for Low Power Applications	California State University Northridge
p21	A4.pdf	Fast Transient Response and High PSRR Low Drop-Out Voltage Regulator	San Jose State University
Power			
p41	P1.pdf	Digital Implementation of Constant Power Load (CPL), Active Resistive, Constant Current and Combinations	University of Texas at Dallas
p19	P2.pdf	High PSRR Low Drop-Out Regulator with Isolated Replica Feedback Ripple Cancellation Technique	University of Texas at Dallas
p7	P3.pdf	Low-Cost Non-Intrusive Device Identification System	Solarillion Foundation
p15	P4.pdf	Low-Power LLC Resonant AC-DC Converter for Phone Charging Applications	University of Texas at Dallas
RF			
p29	R1.pdf	A 3.25 GS/s 4-Tap Analog FIR Filter Design with Coefficient Control using 6-bit Split-Capacitor DAC as a Tunable Coefficient Multiplier	Virginia Tech
p46	R2.pdf	A Full-Duplex Transceiver Employing Inverse Class-D Power Amplifiers	University of Texas at Austin
p22	R3.pdf	A Study of the Low Frequency Noise (LFN) in Reference Injected Phase Locked Loops (RI-PLLs)	The Ohio State University
p3	R4.pdf	A Low-Power RF Receiver Front-End Chip Designed with Methods to Reduce Third-Order Intermodulation Distortion	Northeastern University
p34	R5.pdf	Wide Band Programmable True Time Delay Block for Phased Array Antenna Applications	Arizona State University
Sensors			
p26	S1.pdf	Contactless Hand Tremor Detector based on an Inductive Sensor	University of Texas at Arlington
p28	S2.pdf	A 52 ps Resolution ILO-Based Time-to-Digital Converter Array for LIDAR Sensors - BEST STUDENT PAPER	Texas A&M University, College Station
p1	S3.pdf	Fast Transient Digitizer Chip For Capturing Single-Shot Events	University of Nevada, Las Vegas
p10	S4.pdf	Neural Network Based Real-time Heart Sound Monitor Using a Wireless Wearable Wrist Sensor	University of Texas at Arlington
Digital			
p37	D1.pdf	Traffic-Aware Power-Gating Scheme for Network-On-Chip Routers	University of Louisiana, Infolink-USA
p47	D2.pdf	Red Baron: Near/Post-Silicon SoC Cache Coherence Stress Tester	Texas Instruments, Dallas
p27	D3.pdf	Memristor Based High Fan-out Logic Gates	University of Texas at Austin
Posters			
p45	B1.pdf	Thermal Modeling of the Non-Linear Thermal Resistance of the SiGe HBT using the HICUM Model	University of Texas at Arlington
p16	B2.pdf	Memory Circuits using NanoFerroic Devices	University of Texas at Dallas
p43	B3.pdf	Signal Integrity Analysis of Serpentine Traces in IC Packages	Texas Instruments, Dallas
p44	B4.pdf	A Linear High Gain Time Difference Amplifier Using Feedback Gain Control	University of Nevada, Las Vegas
p9	B5.pdf	A switched capacitor based transimpedance amplifier for detection of HAB using an optical sensor	University of Texas at Arlington
p8	B6.pdf	A low power, low noise Preamplifier for Hearing Aids	University of Texas at Arlington
p48	B7.pdf	A Radiation-Hardened DLL with Fine Resolution and Duty Cycle Corrector for Memory Interface	University of Houston
p38	B8.pdf	High Frequency Noise Characterization of BiCMOS Transistors over Temperature	University of Texas at Dallas
p17	B9.pdf	Characterization of Active Inductors with Modified Determinant Expansion Analysis	San Jose State University