2016 Joint International EUROSOI Workshop and International Conference on Ultimate Integration on Silicon (EUROSOI-ULIS 2016)

Wien, Vienna, Austria
25 – 27 January 2016

IEEE Catalog Number: CFP1649D-POD
ISBN: 978-1-4673-8610-4

******This publication is a representation of what appears in the IEEE Digital Libraries. Some format issues inherent in the e-media version may also appear in this print version.***

IEEE Catalog Number: CFP1649D-POD
ISBN (Print-On-Demand): 978-1-4673-8610-4
ISBN (Online): 978-1-4673-8609-8

Additional Copies of This Publication Are Available From:

Curran Associates, Inc
57 Morehouse Lane
Red Hook, NY 12571 USA
Phone: (845) 758-0400
Fax: (845) 758-2633
E-mail: curran@proceedings.com
Web: www.proceedings.com

2016 Joint International EUROSOI Workshop and International Conference on Ultimate Integration on Silicon

Session I: SOI for Low Power and Sensors

Session II: Tunnel FETs

Session III: Semiconductor Processing

Session IV: Frequency Phenomena and Noise

Session V: Modeling 1

Session VI: Advanced Memory

Session VII: Design, Characterization, and Performance

Session VIII: Modeling 2

Session IX: Advanced Devices and Three-Dimenisonal Integration

Session X: End of Scaling and Beyond CMOS

Session XI: Advanced Devices

Session XII: Posters