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<sup>1</sup> Southern Methodist University Dallas, USA

<sup>2</sup> University of Connecticut Farmington, USA

<sup>3</sup> Inha University, Korea

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<sup>3</sup>Northeastern University, USA

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<sup>1</sup> V.F.S.T.R University, India.

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<sup>1</sup>Okayama Prefectural University, Japan

<sup>2</sup>Kawasaki University of Medical Welfare, Japan

<sup>3</sup>Mitsubishi Motors Corporation, Japan

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<sup>1</sup>Hanbat National University, Korea

<sup>2</sup>Korea Advanced Institute of Science and Technology, Korea

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<sup>1</sup> Xi'an Jiaotong-Liverpool Univeristy, China

<sup>2</sup> Daegu University, Korea

Bio Sensing, Bio Mimicking, and Bio Inspired Circuits and Systems

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Chanmin Park<sup>1</sup>, Injun Park<sup>1</sup>, Woojin Jo<sup>1</sup>, Jimin Cheon<sup>2</sup> and Youngcheol Chae<sup>1</sup> <sup>1</sup>Yonsei University, Korea <sup>2</sup>Kumoh National Institute of Technology, Korea

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Hyeon-June Kim<sup>1,2</sup>, Sun-II Hwang<sup>1</sup>, Jae-Hyun Chung<sup>1</sup> and Seung-Tak Ryu<sup>1</sup> <sup>1</sup>KAIST, Korea <sup>2</sup>SK Hynix, Korea

# [SS4-5] Advanced CMOS Image Sensors for Scientific, Biomedical, and Industrial Imaging Applications N/A

Min-Woong Seo, Keita Yasutomi, Keiichiro Kagawa, and Shoji Kawahito Shizuoka University, Japan,

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# [SS5-1] A 0.45 V 100-Channel Neural-Recording IC With Sub-μW/Channel Consumption in 0.18-μm CMOS N/A

Dong Han<sup>1</sup>, Yuanjin Zheng<sup>1</sup>, Ramamoorthy Rajkumar<sup>2</sup>, Gavin Stewart Dawe<sup>2</sup> and Minkyu Je<sup>3</sup> <sup>1</sup>Nanyang Technological University, Singapore <sup>2</sup>National University of Singapore, Singapore <sup>3</sup>KAIST, Daejeon, Korea

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### [SS6-2] Low-Power Successive-Cancellation Decoding Architecture for Polar Codes N/A

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