

# **ESSCIRC 2018 – IEEE 44th European Solid State Circuits Conference (ESSCIRC 2018)**

**Dresden, Germany  
3 – 6 September 2018**



**IEEE Catalog Number: CFP18542-POD  
ISBN: 978-1-5386-5405-7**

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|-------------------------|-------------------|
| IEEE Catalog Number:    | CFP18542-POD      |
| ISBN (Print-On-Demand): | 978-1-5386-5405-7 |
| ISBN (Online):          | 978-1-5386-5404-0 |
| ISSN:                   | 1930-8833         |

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# ESSCIRC 2018 Table of Contents

## **A1L-D Joint Plenary 1: Udo-Martin Gomez**

Date: Tuesday, September 4, 2018

Time: 09:00 - 09:40

Room: TRE/PHYS/E

**Smart Connected Sensors – Enablers for the IoT..... 1**

Udo Gómez

*Robert Bosch GmbH, Germany*

## **A2L-D Joint Plenary 2: Gary Patton**

Date: Tuesday, September 4, 2018

Time: 09:40 - 10:20

Room: TRE/PHYS/E

**Unleashing Technology Solutions for a New Era of Connected Intelligence ..... 2**

Gary L. Patton

*GLOBALFOUNDRIES, United States*

## **B1L-D Joint Plenary 3: Baher Haroun**

Date: Wednesday, September 5, 2018

Time: 08:40 - 09:20

Room: TRE/PHYS/E

**Autonomous Vehicles Sensor Needs ..... 3**

Baher Haroun

*Texas Instruments, United States*

## **C1L-D Joint Plenary 4: Evangelos S. Eleftheriou**

Date: Thursday, September 6, 2018

Time: 09:00 - 09:40

Room: TRE/PHYS/E

**"In-Memory Computing": Accelerating AI Applications ..... 4**

Evangelos Eleftheriou

*IBM Research, Switzerland*

**A5L-A ESSCIRC Keynote 1: Borivoje Nikolić**

Date: Tuesday, September 4, 2018

Time: 15:50 - 16:30

Room: TRE/MATH/H

**Generating the Next Wave of Custom Silicon ..... 6**

Borivoje Nikolić, Elad Alon, Krste Asanović

*University of California, Berkeley, United States*

**B3L-A ESSCIRC Keynote 2: Georg Sigl**

Date: Wednesday, September 5, 2018

Time: 13:00 - 13:40

Room: TRE/MATH/H

**Where Technology Meets Security: Key Storage and Data Separation for System-on-Chips ..... 12**

Georg Sigl<sup>2</sup>, Mathieu Gross<sup>1</sup>, Michael Pehl<sup>1</sup>

<sup>1</sup>Technical University of Munich, Germany; <sup>2</sup>Technical University of Munich & Fraunhofer Institute for Applied & Integrated Security, Germany

**C3L-A ESSCIRC Keynote 3: Aaron Partridge**

Date: Thursday, September 6, 2018

Time: 13:30 - 14:10

Room: TRE/MATH/H

**It Is Time for Time Itself to Come from Silicon..... N/A**

Aaron Partridge

*SiTime Corporation, United States*

## **A3L-A SAR ADCs**

Date: Tuesday, September 4, 2018  
Time: 11:00 - 12:00  
Room: TRE/MATH/H  
Chair(s): Andrea Baschiroto; *University of Milano-Bicocca*  
Dieter Draxelmayr; *infineon*

### **A 0.1nW-1 $\mu$ W All-Dynamic Capacitance-to-Digital Converter with Power/Speed/Capacitance Scalability ..... 18**

Haoming Xin<sup>1</sup>, Martin Andraud<sup>2</sup>, Peter Baltus<sup>1</sup>, Eugenio Cantatore<sup>1</sup>, Pieter Harpe<sup>1</sup>  
<sup>1</sup>*Eindhoven University of Technology, Netherlands*; <sup>2</sup>*KU Leuven, Belgium*

### **A 125 MS/s 10.4 ENOB 10.1 fJ/Conv-Step Multi-Comparator SAR ADC with Comparator Noise Scaling in 65nm CMOS..... 22**

Shaolong Liu<sup>1</sup>, Jeyanandh Paramesh<sup>1</sup>, Larry Pileggi<sup>1</sup>, Taimur Rabuske<sup>2</sup>, Jorge Fernandes<sup>2</sup>

<sup>1</sup>*Carnegie Mellon University, United States*; <sup>2</sup>*INESC-ID, Portugal*

### **A 0.02-mm<sup>2</sup> 9-Bit 100-MS/s Charge-Injection Cell Based SAR-ADC in 65-nm LP CMOS ..... 26**

Marcel Runge, Dario Schmock, Philipp Scholz, Georg Boeck, Friedel Gerfers  
*Technische Universität Berlin, Germany*

## **A3L-C Power Amplifiers for IoT, Radar and 5G**

Date: Tuesday, September 4, 2018

Time: 11:00 - 12:00

Room: GER/0038/H

Chair(s): Patrick Reynaert; *KU Leuven*  
Domine Leenaerts; *NXP Semiconductors*

### **A CMOS Polar Single-Supply Class-G SCPA for LTE NB-IoT and Cat-M1 ..... 30**

Elbert Bechthum, Mohieddine El Soussi, Johan Dijkhuis, Paul Mateman, Gert-Jan van Schaik, Arjan Breeschoten, Yao-Hong Liu, Christian Bachmann, Kathleen Philips  
*Holst Centre/imec, Netherlands*

### **A Low-Power, Compact 76-81GHz FMCW Transmitter for Automotive Radar in 22nm FDSOI..... 34**

See Lee, Abdellatif Bellaouar, Sherif Embabi  
*GLOBALFOUNDRIES, United States*

### **A 28-GHz CMOS Broadband Single-Path Power Amplifier with 17.4-dBm P1dB for 5G Phased-Array ..... 38**

Chongyu Yu, Jun Feng, Dixian Zhao  
*Southeast University, China*

## **A4L-A Energy Harvesting**

Date: Tuesday, September 4, 2018

Time: 14:00 - 15:00

Room: TRE/MATH/H

Chair(s): Drago Strle; *University of Ljubljana*  
Ke-Horng Chen; *National Chiao Tung University*

### **Multi-Step Bias-Flip Rectification for Piezoelectric Energy Harvesting ..... 42**

Sundeep Javvaji<sup>1</sup>, Vipul Singhal<sup>2</sup>, Vinod Menezes<sup>2</sup>, Rajat Chauhan<sup>2</sup>, Shanthi Pavan<sup>1</sup>

<sup>1</sup>*Indian Institute of Technology Madras, India*; <sup>2</sup>*Texas Instruments, India*

### **A Fully Integrated Switched-Capacitor Based AC-DC Converter for a 120Vrms Mains Interface ..... 46**

Elly De Pelecijn, Michiel Steyaert

*KU Leuven, Belgium*

### **A Sub-nW Fully Integrated Switched-Capacitor Energy Harvester for Implantable Applications ..... 50**

Hyeonji Lee, Eunsang Jang, Hassan Saif, Yongmin Lee, Minsun Kim, Muhammad

Bilawal Khan, Yoonmyung Lee

*Sungkyunkwan University, Korea*

## **A4L-B Image Sensor Architectures**

Date: Tuesday, September 4, 2018

Time: 14:00 - 15:00

Room: TRE/PHYS/E

Chair(s): Matteo Perenzoni; *Fondazione Bruno Kessler*  
Antoine Dupret; *CEA*

### **A 192 × 128 Time Correlated Single Photon Counting Imager in 40nm CMOS Technology ..... 54**

Robert Henderson<sup>2</sup>, Nick Johnston<sup>2</sup>, Haochang Chen<sup>3</sup>, David Li<sup>3</sup>, Graham Hungerford<sup>1</sup>,  
Richard Hirsch<sup>1</sup>, David McLoskey<sup>1</sup>, Philip Yip<sup>3</sup>, David Birch<sup>3</sup>

<sup>1</sup>*Horiba Jovin Yvon IBH Ltd., United Kingdom*; <sup>2</sup>*University of Edinburgh, United Kingdom*;

<sup>3</sup>*University of Strathclyde, United Kingdom*

### **An Adaptive Frame Image Sensor with Fine-Grained Power Management for Ultra-Low Power Internet of Things Application ..... 58**

Masato Osawa<sup>2</sup>, Shuzo Hiraide<sup>2</sup>, Shunsuke Suzuki<sup>2</sup>, Hideki Kato<sup>2</sup>, Kosei Tamiya<sup>2</sup>,  
Yasunari Harada<sup>2</sup>, Kuba Raczkowski<sup>1</sup>, Jean-Luc Bacq<sup>1</sup>, Peter Van Wesemael<sup>1</sup>, Mingxu  
Liu<sup>1</sup>, Annachiara Spagnolo<sup>1</sup>, Koen De Munck<sup>1</sup>, Stefano Guerrieri<sup>1</sup>, Jonathan Borremans<sup>1</sup>

<sup>1</sup>*imec, Belgium*; <sup>2</sup>*Olympus Corporation, Japan*; <sup>2</sup>*Olympus Corporation, Japan*

### **A 5 Million Frames Per Second 3D Stacked Image Sensor with in-Pixel Digital Storage ..... 62**

Laurent Millet<sup>2</sup>, Margaux Vigier<sup>2</sup>, Gilles Sicard<sup>2</sup>, Nils Margotat<sup>2</sup>, Fabrice Guellec<sup>2</sup>,  
Sebastien Martin<sup>2</sup>, Wilfried Uhring<sup>1</sup>

<sup>1</sup>*ICube Laboratory, University of Strasbourg and CNRS, France*; <sup>2</sup>*Université Grenoble  
Alpes, CEA-Leti, France*



## **A4L-C RF and mm-Wave Transceiver Techniques I**

Date: Tuesday, September 4, 2018

Time: 14:00 - 15:00

Room: GER/0038/H

Chair(s): Francois Rivet; *IMS Bordeaux / Université de Bordeaux*  
Andrea Neviani; *University of Padova*

### **A 0.4-1.0GHz, 47MHop/S Frequency Hopped TXR Front-End with 20dB in-Band Blocker Rejection..... 66**

Naser Mousavi, Zhiheng Wang, Ramesh Harjani  
*University of Minnesota, United States*

### **A 125 pJ/Bit 5 mW 28 GHz Superregenerative Receiver with Automatic Gain Control and Energy Efficient Startup for Burst Mode IoE Applications ..... 70**

Ashwin Raghunathan, Thomas Lee  
*Stanford University, United States*

### **A Reconfigurable Receiver with 38 dB Frequency-Independent Blocker Suppression and Enhanced in-Band Linearity and Power Efficiency ..... 74**

Kuangyuan Ying, Carlos Costa Júnior, Bindi Wang, Dusan Milosevic, Hao Gao, Peter Baltus  
*Eindhoven University of Technology, Netherlands*

## **A6L-A Analog Techniques**

Date: Tuesday, September 4, 2018

Time: 16:40 - 17:40

Room: TRE/MATH/H

Chair(s): Kofi Makinwa; *Delft University of Technology*  
Viola Schaffer; *Texas Instruments*

### **A 1V Bandgap Reference in 7-nm FinFET with a Programmable Temperature Coefficient and an Inaccuracy of $\pm 0.2\%$ from $-45^{\circ}\text{C}$ to $125^{\circ}\text{C}$ ..... 78**

Umanath Kamath<sup>2</sup>, Edward Cullen<sup>2</sup>, John Jennings<sup>2</sup>, Ionut Cical<sup>2</sup>, Darragh Walsh<sup>2</sup>, Peng Lim<sup>2</sup>, Brendan Farley<sup>2</sup>, Robert Bogdan Staszewski<sup>1</sup>

<sup>1</sup>*University College Dublin, Ireland*; <sup>2</sup>*Xilinx, Ireland*

### **A 0.5 V, 650 pW, 0.031%/V Line Regulation Subthreshold Voltage Reference ..... 82**

Yuwei Wang, Ruizhi Zhang, Quan Sun, Hong Zhang

*Xi'an Jiaotong University, China*

### **An Mf Energy Harvesting Receiver with Slow QPSK Control Data Demodulator for Wide Area Low Duty Cycle Applications ..... 86**

Teerasak Lee, Henry Kennedy, Rares Bodnar, William Redman-White

*University of Southampton, United Kingdom*

## **A6L-B Digital Circuits**

Date: Tuesday, September 4, 2018  
Time: 16:40 - 18:00  
Room: TRE/PHYS/E  
Chair(s): Christoph Studer; *Cornell University*  
Makoto Ikeda; *University of Tokyo*

### **34.4Mbps 1.56Tbps/W DEFLATE Decompression Accelerator Featuring Block-Adaptive Huffman Decoder in 14nm Tri-Gate CMOS for IoT Platforms ..... 90**

Sudhir Satpathy, Sanu Mathew, Vikram Suresh, Vinodh Gopal, James Guilford, Mark Anders, Himanshu Kaul, Amit Agarwal, Steven Hsu, Ram Krishnamurthy  
*Intel Corporation, United States*

### **An Instruction Driven Adaptive Clock Phase Scaling with Timing Encoding and Online Instruction Calibration for a Low Power Microprocessor ..... 94**

Tianyu Jia, Russ Joseph, Jie Gu  
*Northwestern University, United States*

### **A 230mV-950mV 2.8Tbps/W Unified SHA256/SM3 Secure Hashing Hardware Accelerator in 14nm Tri-Gate CMOS ..... 98**

Vikram Suresh, Sudhir Satpathy, Sanu Mathew, Mark Anders, Himanshu Kaul, Amit Agarwal, Steven Hsu, Ram Krishnamurthy  
*Intel Corporation, United States*

### **A High-Throughput Low-Power Soft Bit-Flipping LDPC Decoder in 28 nm FD-SOI ..... 102**

Kevin Cushon, Per Larsson-Edefors, Peter Andrekson  
*Chalmers University of Technology, Sweden*

## **A6L-C RF and mm-Wave Transceiver Techniques II**

Date: Tuesday, September 4, 2018

Time: 16:40 - 17:40

Room: GER/0038/H

Chair(s): Francois Rivet; *IMS Bordeaux / Université de Bordeaux*  
Andrea Neviani; *University of Padova*

### **580 $\mu$ W 2.2-2.4Ghz Receiver with +3.3dBm Out-of-Band IIP3 for IoT Applications ..... 106**

Sashank Krishnamurthy, Filip Maksimovic, Ali Niknejad  
*University of California, Berkeley, United States*

### **A 4GHz Instantaneous Bandwidth Low Squint Phased Array Using Sub-Harmonic ILO Based Channelization ..... 110**

Qingrui Meng, Ramesh Harjani  
*University of Minnesota, United States*

### **A Quadrature Phase Detector in 28nm CMOS for Differential mm-Wave Sensing Applications Using Dielectric Waveguides ..... 114**

Bart Philippe, Patrick Reynaert  
*KU Leuven, Belgium*

## **B2L-A DCDC Converters**

Date: Wednesday, September 5, 2018

Time: 9:50 - 11:10

Room: TRE/MATH/H

Chair(s): Johan Janssens; *ON Semiconductor*  
Christoph Sandner; *Infineon Technologies AG*

### **A 16.5W Single-Inductor 4-Channel Multi-Color Output DC-DC Buck LED Driver with Digital Control and 96% Efficiency ..... 118**

Michael Hanhart, Soheil Aghaie, Stefan Dietrich, Tobias Zekorn, Ralf Wunderlich, Stefan Heinen

*RWTH Aachen University, Germany*

### **A Pseudo-Ramp Controlled Three Level Buck Converter with an Auto-Ripple Cancellation Technique for Low Output Voltage Ripple in Sub-Threshold Applications..... 122**

Li-Cheng Chu<sup>1</sup>, Shao-Qi Chen<sup>1</sup>, Ke-Horng Chen<sup>1</sup>, Ying-Hsi Lin<sup>2</sup>, Shian-Ru Lin<sup>2</sup>, Tsung-Yen Tsai<sup>2</sup>

<sup>1</sup>*National Chiao Tung University, Taiwan*; <sup>2</sup>*Realtek Semiconductor Corp., Taiwan*

### **A 50V, 1.45ns, 4.1pJ High-Speed Low-Power Level Shifter for High-Voltage DCDC Converters ..... 126**

Daniel Lutz<sup>2</sup>, Achim Seidel<sup>2</sup>, Bernhard Wicht<sup>1</sup>

<sup>1</sup>*Leibniz University Hannover, Germany*; <sup>2</sup>*Reutlingen University, Germany*

### **A Boost Converter with 3-6V Input and Fast Transient Digital Control Comprising a 90 ns-Latency Live-Tracking Window ADC ..... 130**

Samuel Quenzer-Hohmuth<sup>2</sup>, Steffen Ritzmann<sup>3</sup>, Thoralf Rosahl<sup>3</sup>, Bernhard Wicht<sup>1</sup>

<sup>1</sup>*Leibniz University Hannover, Germany*; <sup>2</sup>*Reutlingen University, Germany*; <sup>3</sup>*Robert Bosch GmbH, Germany*

## **B2L-B Frequency Generation**

Date: Wednesday, September 5, 2018  
Time: 09:50 - 11:30  
Room: TRE/PHYS/E  
Chair(s): Dietmar Kissinger; *Innovative Hearth Products*  
Mike Keaveney; *Analog Devices Inc.*

### **Low-Power QPSK Transmitter Based on an Injection-Locked Power Amplifier..... 134**

Zexi Ji, Saba Zargham, Antonio Liscidini  
*University of Toronto, Canada*

### **A Quad-Core 60 GHz Push-Push 45 nm SOI CMOS VCO with $-101.7$ dBc/Hz Phase Noise at 1 MHz Offset, 19 % Continuous FTR and $-187$ dBc/Hz FoMT..... 138**

Johannes Rimmelspacher<sup>1</sup>, Robert Weigel<sup>1</sup>, Amelie Hagelauer<sup>1</sup>, Vadim Issakov<sup>2</sup>  
<sup>1</sup>*Friedrich-Alexander-Universität Erlangen-Nürnberg, Germany*; <sup>2</sup>*Infineon Technologies AG, Germany*

### **D-Band Surface-Wave Modulator and Signal Source with 40 dB Extinction Ratio and 3.7 mW Output Power in 65 nm CMOS ..... 142**

Yuan Liang<sup>4</sup>, Hao Yu<sup>5</sup>, Chirn Chye Boon<sup>3</sup>, Chenyang Li<sup>3</sup>, Dietmar Kissinger<sup>2</sup>, Yong Wang<sup>1</sup>  
<sup>1</sup>*IHP Microelectronics, Germany*; <sup>2</sup>*IHP Microelectronics / Southern University of Science and Technology, Germany*; <sup>3</sup>*Nanyang Technological University, Singapore*; <sup>4</sup>*Nanyang Technological University / IHP Microelectronics, Singapore*; <sup>5</sup>*Southern University of Science and Technology, China*

### **A 4.0–10.0-Gb/s Referenceless CDR with Wide-Range, Jitter-Tolerant, and Harmonic-Lock-Free Frequency Acquisition Technique..... 146**

Jahoon Jin, Jaekwon Kim, Hyeran Kim, Canxing Piao, Jaemin Choi, Dong-Seok Kang, Jung-Hoon Chun  
*Sungkyunkwan University, Korea*

### **A 2.5 ppm/°C 1.05 MHz Relaxation Oscillator with Dynamic Frequency-Error Compensation and 8 $\mu$ s Start-Up Time ..... 150**

Ningxi Liu<sup>2</sup>, Rishika Agarwala<sup>2</sup>, Anjana Dissanayake<sup>2</sup>, Daniel Truesdell<sup>2</sup>, Sumanth Kamineni<sup>2</sup>, Xing Chen<sup>1</sup>, David Wentzloff<sup>1</sup>, Benton Calhoun<sup>2</sup>  
<sup>1</sup>*University of Michigan, United States*; <sup>2</sup>*University of Virginia, United States*

## **B2L-C Machine Learning and Neuromorphic Processors**

Date: Wednesday, September 5, 2018  
Time: 09:50 - 11:30  
Room: GER/0038/H  
Chair(s): Mark Anders; *Intel Corp.*  
Viktor Owall; *Lund University*

### **A 0.78- $\mu$ W 96-Ch. Deep Sub-Vt Neural Spike Processor Integrated with a Nanowatt Power Management Unit..... 154**

Jiangyi Li<sup>1</sup>, Pavan K. Chundi<sup>1</sup>, Sung Kim<sup>1</sup>, Zhewei Jiang<sup>1</sup>, Minhao Yang<sup>1</sup>, Joongseong Kang<sup>2</sup>, Seungchul Jung<sup>2</sup>, Sang Joon Kim<sup>2</sup>, Mingoo Seok<sup>1</sup>

<sup>1</sup>*Columbia University, United States*; <sup>2</sup>*Samsung Electronics, Korea*

### **A Wide Dynamic Range Sparse FC-DNN Processor with Multi-Cycle Banked SRAM Read and Adaptive Clocking in 16nm FinFET..... 158**

Sae Kyu Lee<sup>3</sup>, Paul Whatmough<sup>2</sup>, Niamh Mulholland<sup>1</sup>, Patrick Hansen<sup>1</sup>, David Brooks<sup>1</sup>, Gu-Yeon Wei<sup>1</sup>

<sup>1</sup>*Harvard University, United States*; <sup>2</sup>*Harvard University / Arm Research, United States*;

<sup>3</sup>*Harvard University / IBM Research, United States*

### **A 0.23mW Heterogeneous Deep-Learning Processor Supporting Dynamic Execution of Conditional Neural Networks ..... 162**

Hsi-Shou Wu<sup>2</sup>, Zhengya Zhang<sup>2</sup>, Marios Papaefthymiou<sup>1</sup>

<sup>1</sup>*University of California, Irvine, United States*; <sup>2</sup>*University of Michigan, United States*

### **Laika: a 5uW Programmable LSTM Accelerator for Always-on Keyword Spotting in 65nm CMOS..... 166**

Juan Sebastian P. Giraldo<sup>1</sup>, Marian Verhelst<sup>2</sup>

<sup>1</sup>*KU Leuven, Belgium*; <sup>2</sup>*KU Leuven ESAT-MICAS, Belgium*

### **iFPNA: a Flexible and Efficient Deep Neural Network Accelerator with a Programmable Data Flow Engine in 28nm CMOS..... 170**

Chixiao Chen, Xindi Liu, Huwan Peng, Hongwei Ding, C-J. Richard Shi  
*University of Washington, United States*

## **B4L-A    Advanced Wireline Techniques**

Date:        Wednesday, September 5, 2018

Time:        13:50 - 15:10

Room:       TRE/MATH/H

Chair(s):    Filip Tavernier; *KU Leuven*  
                 Sven Mattisson; *Ericsson*

### **A 50-Gb/s Quarter-Rate Voltage-Mode Transmitter with Three-Tap FFE in 40-nm CMOS ..... 174**

Pen-Jui Peng, Yan-Ting Chen, Chao-Hsuan Chen, Sheng-Tsung Lai, Hsiang-En Huang,  
Ho-Hsuan Lu, Tsai-Chin Yu  
*Yuan-Ze University, Taiwan*

### **A Simultaneous Bidirectional Single-Ended Coaxial Link with 24-Gb/s Forward and 312.5-Mb/s Back Channels ..... 178**

Abishek Manian, Amit Rane, Yongseon Koh  
*Texas Instruments, United States*

### **A 19Gb/s RX for VSR-C2C Links with Clock-Less DFE and High-BW CDR Based on Master-Slave ILOs in 14nm CMOS ..... 182**

Gautam Gangasani, Peter Kinget  
*Columbia University, United States*

### **A 3-12.5 Gb/s Reference-Less CDR for an Eye-Opening Monitor ..... 186**

Bob Schell, Robert Bishop, Jack Kenney  
*Analog Devices, Inc., United States*



## **B4L-B Capacitive Sensor Interfaces**

Date: Wednesday, September 5, 2018

Time: 13:50 - 14:50

Room: TRE/PHYS/E

Chair(s): Christian Enz; *École Polytechnique Fédérale de Lausanne*  
Robert Henderson; *University of Edinburgh*

### **A 15nW Per Button Noise-Immune Readout IC for Capacitive Touch Sensor ..... 190**

Said Hussaini<sup>2</sup>, Hui Jiang<sup>2</sup>, Paul Walsh<sup>1</sup>, Dermot Macsweeney<sup>1</sup>, Kofi Makinwa<sup>2</sup>

<sup>1</sup>Cypress Semiconductor, Ireland; <sup>2</sup>Delft University of Technology, Netherlands

### **In-Panel 31.17dB 140kHz 87µW Unipolar Dual-Gate In-Ga-Zn-O Charge-Sense Amplifier for 500dpi Sensor Array on Flexible Displays..... 194**

Nikolaos Papadopoulos<sup>2</sup>, Soeren Steudel<sup>2</sup>, Florian De Roose<sup>3</sup>, Doaa M. Elgabry<sup>2</sup>, Auke

Jisk Kronemeijer<sup>1</sup>, Jan Genoe<sup>3</sup>, Wim Dehaene<sup>4</sup>, Kris Myny<sup>2</sup>

<sup>1</sup>Holst Centre / TNO, Netherlands; <sup>2</sup>imec, Belgium; <sup>3</sup>imec / KU Leuven, Belgium; <sup>4</sup>KU Leuven / imec, Belgium

### **A Low-Power Auto-Zero Switched-Capacitor Dual-Slope Noise-Shaping Direct CDC ..... 198**

Christopher Rogi<sup>1</sup>, Enrique Prefasi<sup>2</sup>, Richard Gaggl<sup>1</sup>

<sup>1</sup>Infineon Technologies Austria AG, Austria; <sup>2</sup>UC3M, Spain

## **B4L-C LDO Techniques**

Date: Wednesday, September 5, 2018  
Time: 13:50 - 14:50  
Room: GER/0038/H  
Chair(s): Joseph Shor; *Bar-Ilan University*  
Vadim Ivanov; *Texas Instruments*

### **LDO-Assisted Voltage Selector Over 0.5-to-1V VDD Range for Fine Grained DVS in FDSOI 28nm with 200ns/V Controlled Transition..... 202**

Anthony Quelen, Guilherme Migliato Marega, Sylvain Bouquet, Ivan Miro-Panades, Gaël Pillonnet

*CEA-Leti / Université Grenoble Alpes, France*

### **A 0.7V Fully-on-Chip Pseudo-Digital LDO Regulator with 6.3μA Quiescent Current and 100mV Dropout Voltage in 0.18-μm CMOS..... 206**

Junyao Tang, Chenchang Zhan, Guanhua Wang, Yang Liu

*Southern University of Science and Technology, China*

### **A 320μV-Output Ripple and 90ns-Settling Time at 0.5V Supply Digital-Analog-Hybrid LDO Using Multi-Level Gate-Voltage Generator and Fast-Decision PD Detector ..... 210**

Younghyun Lim, Jeonghyun Lee, Yongsun Lee, Seyeon Yoo, Jaehyouk Choi

*Ulsan National Institute of Science and Technology, Korea*

**B4L-E FOCUS SESSION: FD-SOI part I**

Date: Wednesday, September 5, 2018

Time: 13:50 - 15:10

Room: CHE/0089/E

Chair(s): Andreia Cathelin; *STMicroelectronics*

**RF FDSOI Technology and Modelling..... 214**

David Haramé

*GLOBALFOUNDRIES, Germany*

**FD-SOI Integration Solutions for Analog, RF and Millimeter-Wave Applications ..... 215**

Andreia Cathelin

*STMicroelectronics, France*

**FDSOI Circuit Design for High Energy Efficiency: Wide Operating Range and ULP Applications – a 7-Year Experience ..... 216**

Edith Beigne

*CEA-Leti, France*

**Exploiting FDSOI Towards Minimum Energy Point Operation in Processors and Machine Learning Accelerators ..... 217**

Marian Verhelst

*KU Leuven, Belgium*

## **B5L-A Biomedical Sensors**

Date: Wednesday, September 5, 2018

Time: 15:40 - 17:00

Room: TRE/MATH/H

Chair(s): Chris Ruddell; *University of Washington*  
Hanspeter Schmid; *FH Nordwestschweiz*

### **A Dual Loop 8-Channel ECG Recording System with Fast Settling Mode ..... 218**

Lei Zeng, Chun Huat Heng

*National University of Singapore, Singapore*

### **A 52 $\mu$ W Heart-Rate Measurement Interface Fabricated on a Flexible Foil with a-IGZO TFTs..... 222**

Mohammad Zulqarnain<sup>1</sup>, Stefano Stanzione<sup>2</sup>, Jan-Laurens P.J. van der Steen<sup>3</sup>, Gerwin H. Gelinck<sup>3</sup>, Kris Myny<sup>4</sup>, Sahel Abdinia<sup>1</sup>, Eugenio Cantatore<sup>1</sup>

<sup>1</sup>*Eindhoven University of Technology, Netherlands;* <sup>2</sup>*Holst Centre/imec, Netherlands;*

<sup>3</sup>*Holst Centre/TNO, Netherlands;* <sup>4</sup>*imec, Belgium*

### **All Wireless, 16-Channel Epilepsy Control System with Sub- $\mu$ W/Channel and Closed-Loop Stimulation Using a Switched-Capacitor-Based Active Charge Balancing Method ..... 226**

Reza Ranjandish<sup>1</sup>, Kerim Ture<sup>1</sup>, Franco Maloberti<sup>2</sup>, Catherine Dehollain<sup>1</sup>, Alexandre Schmid<sup>1</sup>

<sup>1</sup>*Swiss Federal Institute of Technology, Switzerland;* <sup>2</sup>*University of Pavia, Italy*

### **A CMOS Distributed Sensor System for High-Density Wireless Neural Implants for Brain-Machine Interfaces..... 230**

Vincent Leung<sup>2</sup>, Jihun Lee<sup>1</sup>, Siwei Li<sup>2</sup>, Siyuan Yu<sup>2</sup>, Chester Kilfoyle<sup>1</sup>, Lawrence Larson<sup>1</sup>, Arto Nurmikko<sup>1</sup>, Farah Laiwalla<sup>1</sup>

<sup>1</sup>*Brown University, United States;* <sup>2</sup>*University of California, San Diego, United States*

## **B5L-B Optical and Waveguide Transceivers**

Date: Wednesday, September 5, 2018

Time: 15:40 - 16:40

Room: TRE/PHYS/E

Chair(s): Thomas Toifl; *IBM Zurich*  
Filip Tavernier; *KU Leuven*

### **A D-Band Foam-Cladded Dielectric Waveguide Communication Link with Automatic Tuning..... 234**

Yang Zhang, Maxime de Wit, Patrick Reynaert  
*KU Leuven, Belgium*

### **A 112 Gb/s PAM4 Linear TIA with 0.96 pJ/Bit Energy Efficiency in 28 nm CMOS ..... 238**

Hao Li, Ganesh Balamurugan, James Jaussi, Bryan Casper  
*Intel Corporation, United States*

### **A 1310/1550 nm Fully-Integrated Optical Receiver with Schottky Photodiode and Low-Noise Transimpedance Amplifier in 40 nm Bulk CMOS ..... 242**

Wouter Diels, Michiel Steyaert, Filip Tavernier  
*KU Leuven, Belgium*

## **B5L-C    ADC for Communications**

Date: Wednesday, September 5, 2018

Time: 15:40 - 16:40

Room: GER/0038/H

Chair(s): Piero Malcovati; *University of Pavia*

Qiang Li; *University of Electronic Science and Technology of China*

### **A Multi-Mode GSM to LTE100 ADC ..... 246**

Jens Sauerbrey<sup>2</sup>, Jacinto San Pablo Garcia<sup>2</sup>, Udo Schütz<sup>2</sup>, Hasham Khushk<sup>1</sup>, John G. Kauffman<sup>3</sup>

<sup>1</sup>Intel Corporation, United States; <sup>2</sup>Intel Deutschland GmbH, Germany; <sup>3</sup>Nokia Networks Santa Clara California, United States

### **Clock Synchronous Reset and Skew Calibration of 65GS/s ADCs in a Multi-Lane Coherent Receiver..... 250**

Shankarram Athreya<sup>2</sup>, Hiva Hedayati<sup>2</sup>, Shayan Kazemkhani<sup>2</sup>, Yanfei Chen<sup>1</sup>, Saurabh Vats<sup>2</sup>, Michael Scott<sup>2</sup>, Bart Zeydel<sup>2</sup>, Peter Keller<sup>2</sup>, Jian Wang<sup>2</sup>, Bhaskarareddy Avula<sup>2</sup>, Boris Murmann<sup>3</sup>, Echere Iroaga<sup>2</sup>

<sup>1</sup>Intel Corporation, United States; <sup>2</sup>MACOM Technology Solutions, United States;

<sup>3</sup>Stanford University, United States

### **A 39mW 7b 8GS/s 8-Way TI ADC with Cross-Linearized Input and Bootstrapped Sampling Buffer Front-End..... 254**

Chi-Hang Chan, Yan Zhu, Zihao Zheng, Rui Paulo da Silva Martins

*University of Macau, Macau*

**B5L-E FOCUS SESSION: FD-SOI part II**

Date: Wednesday, September 5, 2018

Time: 15:40 - 17:00

Room: CHE/0089/E

Chair(s): Andreia Cathelin; *STMicroelectronics*

**Energy-Efficient Design in FDSOI ..... 258**

Borivoje Nikolić

*University of California, Berkeley, United States*

**mmW and High Speed Solutions Enabled by FD-SOI..... 259**

Sorin Voinigescu

*University of Toronto, Canada*

**Circuit Design Challenges of Highly-Integrated mm-Wave Radar-Based Sensors in SOI Based Technologies ..... 260**

Vadim Issakov

*Infineon, Germany*

**FD-SOI Enabled mmWave Telecommunication Applications and System Architectures ..... 261**

Anirban Bandyopadhyay

*GlobalFoundries, United States*

**C2L-A FOCUS SESSION: Power Electronics**

Date: Thursday, September 6, 2018

Time: 10:20 - 11:40

Room: TRE/MATH/H

Chair(s): Rolf Weis; *Infineon*  
Giovanni Frattini; *Texas Instruments*

**Gate Driver Solutions for Modern Power Devices and Topologies ..... 262**

Reinhard Herzer

*SEMIKRON Elektronik GmbH & Co KG, Germany*

**Noise or Power Loss? – a Dilemma in Driving GaN Power Transistors .....No Paper**

Brian Ma

*University of Texas at Dallas, United States*

**Integration of Switching Power Converters: Challenge Ahead.....No Paper**

Gerard Villar Piqué, Ravi Karadi

*NXP Semiconductors, Netherlands*

**HV Floating Switch Matrix with Parachute Safety Driving for 3D Echography  
Systems ..... 271**

Giulio Ricotti, Valeria Bottarel

*STMicroelectronics, Italy*



## **C2L-B Low Power Processors for IOT**

Date: Thursday, September 6, 2018

Time: 10:20 - 11:40

Room: TRE/PHYS/E

Chair(s): Wim Dehaene; *KU Leuven*

Andreas Burg; *École Polytechnique Fédérale de Lausanne*

### **Mr.Wolf: a 1 GFLOP/s Energy-Proportional Parallel Ultra Low Power SoC for IoT Edge Processing ..... 274**

Antonio Pullini<sup>1</sup>, Davide Rossi<sup>3</sup>, Igor Loi<sup>2</sup>, Alfio Di Mauro<sup>1</sup>, Luca Benini<sup>1</sup>

<sup>1</sup>*ETH Zürich, Switzerland*; <sup>2</sup>*Greenwaves Technologies, France*; <sup>3</sup>*University of Bologna, Italy*

### **A Cortex-M3 Based MCU Featuring AVS with 34nW Static Power, 15.3pJ/inst. Active Energy, and 16% Power Variation Across Process and Temperature ..... 278**

Ruben Salvador<sup>1</sup>, Alberto Sanchez<sup>1</sup>, Xin Fan<sup>2</sup>, Tobias Gemmeke<sup>2</sup>

<sup>1</sup>*Analog Devices, Inc., Spain*; <sup>2</sup>*IDS RWTH Aachen University, Germany*

### **Flexible and Self-Adaptive Sense-and-Compress for Sub-Microwatt Always-on Sensory Recording ..... 282**

Jaro De Roose<sup>4</sup>, Haoming Xin<sup>1</sup>, Martin Andraud<sup>2</sup>, Pieter Harpe<sup>1</sup>, Marian Verhelst<sup>3</sup>

<sup>1</sup>*Eindhoven University of Technology, Netherlands*; <sup>2</sup>*KU Leuven, Belgium*; <sup>3</sup>*KU Leuven ESAT-MICAS, Belgium*; <sup>4</sup>*Micas, KU Leuven, Belgium*

### **A Dual-Mode NB-IoT and EC-GSM RF-SoC Achieving -128-dBm Extended-Coverage and Supporting OTDOA and a-GPS Positioning..... 286**

Matthias Korb<sup>1</sup>, Samuel Willi<sup>1</sup>, Benjamin Weber<sup>1</sup>, Harald Kröll<sup>1</sup>, Andreas Traber<sup>1</sup>, Stefan Altorfer<sup>1</sup>, David Tschopp<sup>1</sup>, Jürgen Rogin<sup>1</sup>, Edwin Dornbierer<sup>1</sup>, Mauro Salomon<sup>2</sup>, Stefan Lippuner<sup>2</sup>, Lianbo Wu<sup>2</sup>, Qiuting Huang<sup>2</sup>

<sup>1</sup>*ACP Advanced Circuit Pursuit AG, Switzerland*; <sup>2</sup>*ETH Zurich, Switzerland*

## **C2L-C Amplifiers and Filters**

Date: Thursday, September 6, 2018  
Time: 10:20 - 11:20  
Room: GER/0038/H  
Chair(s): Marco Berkhout; *NXP Semiconductors*  
Hugo Veenstra; *Philips*

### **A 36V 48MHz JFET-Input Bipolar Operational Amplifier with 150 $\mu$ V Maximum Offset and Overload Supply Current Control ..... 290**

Martijn Snoeij  
*Texas Instruments, Germany*

### **An Integrator-Differentiator TIA Using a Multi-Element Pseudo-Resistor in its DC Servo Loop for Enhanced Noise Performance..... 294**

Matthias Häberle<sup>4</sup>, Denis Djekic<sup>3</sup>, George Fantner<sup>1</sup>, Klaus Lips<sup>2</sup>, Maurits Ortmanns<sup>4</sup>, Jens Anders<sup>3</sup>  
<sup>1</sup>*École Polytechnique Fédérale de Lausanne, Switzerland*; <sup>2</sup>*Helmholtz-Zentrum Berlin for Materials and Energy, Germany*; <sup>3</sup>*University of Stuttgart, Germany*; <sup>4</sup>*University of Ulm, Germany*

### **A 28nm-CMOS 100MHz 1mW 12dBm-IIP3 4th-Order Flipped-Source-Follower Analog Filter ..... 298**

Federico Fary<sup>2</sup>, Marcello De Matteis<sup>2</sup>, Tommaso Vergine<sup>1</sup>, Andrea Baschiroto<sup>2</sup>  
<sup>1</sup>*Microelectronics Group CERN, Switzerland*; <sup>2</sup>*University of Milano-Bicocca, Italy*

## **C4L-A Low Power Wireless**

Date: Thursday, September 6, 2018

Time: 14:20 - 15:40

Room: TRE/MATH/H

Chair(s): Giuseppe Gremagna; *Huawei*  
Takashi Oshima; *Hitachi Global*

### **A 470 $\mu$ W -92.5dBm OOK/FSK Receiver for IEEE 802.11 WiFi LP-WUR ..... 302**

Jaeho Im, Hun-Seok Kim, David Wentzloff  
*University of Michigan, United States*

### **Quantized Analog RX Front-End for SAW-Less Applications..... 306**

Javid Musayev, Antonio Liscidini  
*University of Toronto, Canada*

### **A 0.85mm<sup>2</sup> BLE Transceiver with Embedded T/R Switch, 2.6mW Fully-Passive Harmonic Suppressed Transmitter and 2.3mW Hybrid-Loop Receiver..... 310**

Zheng Sun, Hanli Liu, Dexian Tang, Hongye Huang, Tohru Kaneko, Rui Wu, Wei Deng,  
Kenichi Okada  
*Tokyo Institute of Technology, Japan*

### **A 15mW -105dBm Image-Sparse-Sliding-If Receiver with Transformer-Based on- Chip Q-Enhanced RF Matching Network for a 113dB-Link-Budget BLE 5.0 TRX ..... 314**

Tuan Thanh Ta<sup>1</sup>, Yosuke Ogasawara<sup>2</sup>, Tong Wang<sup>2</sup>, Masayoshi Oshiro<sup>2</sup>, Naotaka  
Koide<sup>2</sup>, Akihide Sai<sup>1</sup>, Takashi Tokairin<sup>2</sup>  
<sup>1</sup>*Toshiba Corporation, Japan*; <sup>2</sup>*Toshiba Electronic Devices & Storage Corporation, Japan*

## **C4L-B Digital Processors**

Date: Thursday, September 6, 2018  
Time: 14:20 - 15:20  
Room: TRE/PHYS/E  
Chair(s): Sylvain Clerc; *STMicroelectronics*  
Atila Alvandpour; *Linköping University*

### **A 29 Gops/Watt 3D-Ready 16-Core Computing Fabric with Scalable Cache Coherent Architecture Using Distributed L2 and Adaptive L3 Caches ..... 318**

Eric Guthmuller<sup>3</sup>, César Fuguet<sup>3</sup>, Pascal Vivet<sup>3</sup>, Christian Bernard<sup>3</sup>, Ivan Miro-Panades<sup>1</sup>, Jean Durupt<sup>3</sup>, Edith Beigne<sup>3</sup>, Didier Lattard<sup>3</sup>, Séverine Cheramy<sup>3</sup>, Alain Greiner<sup>2</sup>, Quentin Meunier<sup>2</sup>, Pirouz Bazargan Sabet<sup>2</sup>

<sup>1</sup>CEA-Leti / Université Grenoble Alpes, France; <sup>2</sup>Sorbonne Université, CNRS, LIP6, France; <sup>3</sup>Université Grenoble Alpes, CEA-Leti, France

### **A Real-Time, Analog/Digital Co-Designed 1.89-GHz Bandwidth, 175-Khz Resolution Sparse Spectral Analysis RISC-V SoC in 16-nm FinFET ..... 322**

Angie Wang, Woorham Bae, Jaeduk Han, Stevo Bailey, Paul Rigge, Orhan Ocal, Zhongkai Wang, Kannan Ramchandran, Elad Alon, Borivoje Nikolić  
*University of California, Berkeley, United States*

### **A Sub 10 pJ/Cycle Over a 2 to 200MHz Performance Range RISC-V Microprocessor in 28 nm FDSOI ..... 326**

Roel Uytterhoeven<sup>1</sup>, Wim Dehaene<sup>2</sup>

<sup>1</sup>KU Leuven, Belgium; <sup>2</sup>KU Leuven / imec, Belgium

## **C4L-C Magnetic and Thermal Sensors**

Date: Thursday, September 6, 2018

Time: 14:20 - 15:40

Room: GER/0038/H

Chair(s): Eugenio Cantatore; *Eindhoven University of Technology*

### **A Single-Temperature-Calibration 0.18- $\mu$ m CMOS Time-Based Resistive Sensor Interface with Low Drift Over a -40°C to 175°C Temperature Range ..... 330**

Jorge Marin<sup>1</sup>, Elisa Sacco<sup>1</sup>, Johan Vergauwen<sup>2</sup>, Georges Gielen<sup>1</sup>

<sup>1</sup>KU Leuven, Belgium; <sup>2</sup>Melexis, Belgium

### **Ultra-Miniature 0.003 mm<sup>2</sup> PNP-Based Thermal Sensor for CPU Thermal Monitoring ..... 334**

Ori Bass, Joseph Shor

*Bar-Ilan University, Israel*

### **A Signal and Offset T&H Frontend for Spinning Hall Sensors with Ping-Pong and Chopping Techniques..... 338**

Yongjia Li, Mario Motz, Leneesh Raghavan

*Infineon Technologies Austria AG, Austria*

### **A Coupled Inductive Bridge for Magnetic Sensing Applications..... 342**

Matan Gal-Katziri, Ali Hajimiri

*California Institute of Technology, United States*