2018 IEEE 36th International Conference on Computer Design (ICCD 2018)

Orlando, Florida, USA
7-10 October 2018

Additional Copies of This Publication Are Available From:

2018 IEEE 36th International Conference on Computer Design

ICCD 2018

Table of Contents

Session 1: Best Papers Session

Session 2A: SSD

Session 2B: Side Channels

Session 3A: Security and Capability

Session 3B: Microarchitecture

Session 4A: Logic and Circuit Design 1

Session 4B: Design Automation

Session 5A: Novel Architectures

Session 5B: Memory 1

Session 6A: Memory 2

Session 6B: Logic and Circuit Design 2

Session 7A: Accelerators and GPUs

Session 7B: Potpouri 1

Session 8A: NVM

Session 8B: Test and Verification

Session 9A: Network on Chip and Synchronization

Session 9B: Potpouri 2

Session 10A: File System and Cloud

Session 10B: FPGA and Machine Learning