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Technical Sessions (Oral Sessions)

16:00-17:20 ***Friday, May 24***

Conference Room	**ORAL I DEVICE** Chairs: Prof. Yue-Ming Hsin, *National Central University, Taiwan* Prof. Chih-Fang Huang, *National Tsing-Hua University, Taiwan*
A1-1	***Invited Paper: Vertical leakage in GaN-on-Si high-voltage transistors: Phenomena involved, TCAD modelling and suppression technique*** N/A Giorgia Longobardi Cambridge Graphene Centre, Engineering Department, University of Cambridge, UK
A1-2	***Oxide Breakdown Reliability of SiC MOSFET*** 1 Cheng-Tyng Yen[a], H.Y. Lee[b], C. C.Hung[a], C. Y. Lee, L. S. Lee F. J. Hsu, K. T. Chu [a] Hestia Power Inc., Hsinchu, Taiwan [b] Industrial Technology Research Institute, Hsinchu, Taiwan
A1-3	***Invited Paper: Advanced tools which applicate in Compound Semiconductor*** N/A Richard Teng Scientech Corporation
A1-4	***Diode-Integrated SiC DMOSFETs*** N/A Siddarth Sundaresan[a], Stanley Atcitty[b], Vamsi Mulpuri[a] and Ranbir Singh[a] [a] GeneSiC Semiconductor, Dulles, VA, USA [b] Sandia National Laboratories, Albuquerque, NM, USA

Room 101	**ORAL I APPLICATION** Chairs: Prof. Katherine Kim, *National Taiwan University, Taiwan* Prof. Laili Wang, Xi'an Jiaotong University, China
B1-1	***Accuracy Analysis of Calorimetric Loss Measurement for Benchmarking Wide Bandgap Power Transistors under Soft-Switching Operation*** 18 Dominik Koch[a], Samuel Araujo[b], and Ingmar Kallfass[a] [a] Institute of Robust Power Semiconductor Systems, University of Stuttgart, Germany [b] Robert Bosch GmbH, Renningen, Germany
B1-2	***A Temperature-dependent PSpice Short-circuit Model of SiC MOSFET*** 24 Xingran Zhao[a], Hong Li [a], Chao Feng [a], Zhe Zhou [b], Kai Sun [c] and Zhengming Zhao [c] [a] School of Electrical Engineering, Beijing Jiaotong University, Beijing, China, [b] Global Energy Interconnection Research Institute, Beijing, China, [c] Department of Electrical Engineering, Tsinghua University, Beijing, China
B1-3	***Modulation strategy of Three Phase Dual Acitve Bridge Converter using the SiC MOSFET for improving light load condition*** 29 Hyunjun Choi [1], Hwapyeong Park [1], and Jeehoon Jung [1] [1] Electrical Engineering, Ulsan National Institute of Science and Technology (UNIST), Ulsan, Republic of Korea.
B1-4	***Loss Analysis of T-type NPC Inverter with Active Power Decoupling Capability Operated in Discontinuous Current Mode*** 34 Akiyoshi Omomo, Jun-ichi Itoh, and Keisuke Kusaka Dept. of Energy and Environmental Science, Nagaoka University of Technology, Nagaoka, Niigata, Japan

Room 103	**ORAL I APPLICATION** Chairs: Prof. Chang-Hua Lin, *National Taiwan University of S&T, Taiwan* Prof. Cheng-Yu Tang, *National Taipei University of Technology, Taiwan*
C1-1	***High Resolution Dynamic Rdson Measurement of GaNFET using Optimized Vdson Measurement Circuit*** 74 Dominik Koch[a], Samuel Araujo[b], and Ingmar Kallfass[a] Arief Noor Rahman, Meng Yang Li, Marojahan Tampubolon, Huang Jen Chiu Department of Electronics and Computer Engineering, NTUST, Taipei, Taiwan
C1-2	***A 10MHz GaN Driver with Gate Ringing Suppression and Active Bootstrap Control*** 78 Sheng Teng Li, Pin Ying Wang, and Ching Jan Chen Power Electronics Laboratory, National Taiwan University, Taipei, Taiwan
C1-3	***Layout Optimization of 2.2kW Motor Drive Inverter with GaN Device Application by the assistance of CAD Software Simulation*** N/A Shiang-Ren Jeng [a], Yaow-Ming Chen [a], and Chih-Chao Hsu [b] [a] Electric Energy Processing Laboratory, Graduate Institute of Electrical Engineering, National Taiwan University, Taipei, Taiwan [b] National Chung-Shan Institute of Science and Technology Taoyuan, Taiwan

Technical Sessions (Oral Sessions)

15:10-16:30 ***Saturday, May 25***

Room 101	**ORAL II DEVICE** Chairs: Dr. Tomohide Terashima, Mitsubishi Electric Corporation, Japan Prof. Chih-Fang Huang, National Tsing-Hua University, Taiwan
A2-1	***Invited Paper: Joint Material for power semiconductors by Cu-Sn Intermetallic Compound (IMC)*** 4 Hiroaki Ikeda Napra, Japan
A2-2	***Demonstration of Annealing-free Metal-Insulator-Semiconductor (MIS) Ohmic Contacts on a GaN Substrate using Low Work-function Metal (Yb) and Al2O3 Interfacial Layer*** 10 Tian-Li Wu [a], Yang-Yan Tseng [a], Chih-Fang Huang [b], Zih-Sin Chen [c], Chih-Chien Lin [c], Chung-Jen Chung [c], Po-Kai Huang [d], and Kuo-Hsing Kao [d] [a] International College of Semiconductor Technology, National Chiao Tung University, Taiwan. [b] Institute of Electronics Engineering, National Tsing Hua University, Taiwan. [c] Center for Micro/Nano Science and Technology, National Cheng Kung University, Taiwan. [d] Department of Electrical Engineering, National Cheng Kung University, Taiwan.
A2-3	***RF Performance of HfO_2 SiNx Dual-Layer Passivation of AlGaN GaN MIS-HEMT*** N/A Chia-Jui Yu, Bo-Kang Lai, Chien-Ju Chen, Jyun-Hao Liao, Meng-Chyi Wu Institute of Electronics Engineering, National Tsing-Hua University, Hsinchu, Taiwan
A2-4	***Defect Inspection and Characterization for WBG Epitaxy Applications*** N/A Edwin Chew KLA-Tencor

Room 103	**ORAL II APPLICATION** Chairs: Prof. Junichi Itoh, *Nagaoka University of Technology, Japan* Prof. Huang-Jen Chiu, *National Taiwan University of S&T, Taiwan*
B2-1	***Invited Paper: Wide Bandgap-based Power Conversion Systems for Grid-tied Energy Storage*** N/A Stanley Atcitty Sandia National Laboratories, US
B2-2	***GaN Based DC-DC Converter for 48 V Automotive Applications*** 38 Suvankar Biswas [a] and Michael de Rooij [b a] Efficient Power Conversion Corporation, Blacksburg, USA [b] Efficient Power Conversion Corporation, El Segundo, USA
B2-3	***Research of PCB Parasitic Inductance in the GaN HEMT Power Loop*** 44 Bainan Sun, Zhe Zhang and Michael A.E. Andersen Technical University of Denmark, Denmark
B2-4	***Optimization Design of EMI Filter with Chaotic PWM in DC-DC Converters*** 49 Hong Li, Yuhang Ding, and Zhichang Yang Beijing Jiaotong University, Beijing, China

Room 101	**ORAL III DEVICE** Chairs: Dr. Giorgia Longobardi, *University of Cambridge, United Kingdom* Prof. Kung-Yen Lee, *National Taiwan University, Taiwan*
A3-1	***RF Performance of HfO2/SiNx Dual-Layer Passivation of AlGaN/GaN MIS-HEMTs Grown on SiC Substrate*** N/A Chia-Jui Yu[1], Bo-Kang Lai[1], Chien-Ju Chen[1], Jyun-Hao Liao[1], Meng-Chyi Wu[1], Yu-Chi Wang[2] [1] Institute of Electronics Engineering, National Tsing-Hua University, Hsinchu, Taiwan [2] WIN Semiconductors Corp., Taoyuan, Taiwan
A3-2	***The Demonstration of Recessed Anodes AlGaN/GaN Schottky Barrier Diodes Using Plasma Oxidation/Wet Etching Techniques*** 14 Kuang-Po Hsueh [a], Hao-Yu Wang[b], Hsien-Chin Chiu [b], Hsiang-Chun Wang [b], Hsuan-Ling Kao [b], Feng-Tso Chien [c], Wen-Yen Lin [d] [a] Department of Digital Multimedia Technology, Vanung University, Chungli, Taiwan [b] Department of Electronics Engineering, Chang Gung University, Taoyuan, Taiwan [c] Department of Electronics Engineering, Feng-Chia University, Taichung, Taiwan [d] Department of Information Management, National Taichung University of Science and Technology, Taichung, Taiwan
A3-3	***Vertical GaN high-electron-mobility transistors on Si substrate using p- GaN/2DEG structure*** N/A Dai-Jie Lin [1] and Jian-Jang Huang[1,2] [1] Graduate Institute of Photonics and Optoelectronics, National Taiwan University, Taipei, Taiwan [2] Department of Electrical Engineering, National Taiwan University, Taipei, Taiwan
A3-4	***The effect of buffer layer on RF characteristics of AlGaN/GaN-on-Si HEMT*** N/A Yu-EnJeng [1], Li-Cheng Chang [2], Wei-ChengTzeng [2], Kai-Chieh Hsu [2], Yung-Ting Ho [1] and Chao-Hsin Wu [1,2] [1] Graduate Institute of Photonics and Optoelectronics, National Taiwan University, Taiwan [2] Graduate Institute of Electronics Engineering, National Taiwan University, Taiwan

Room 103	**ORAL III APPLICATION** Chairs: Dr. Stanley Atcitty, *Sandia National Laboratories, US* Prof. Ching-Jen Chen, *National Taiwan University, Taiwan*
B3-1	***65 W Synchronous Current-Mode Class D Rectifier Using GaN FETs for 6.78 MHz Resonant Wireless Power Receivers*** 55 Yuanzhe Zhang and Michael de Rooij Efficient Power Conversion Corporation, El Segundo, CA, USA
B3-2	***Study on the Imbalanced Voltage of Series-Connected Active Power Semiconductor Devices in Power Conversion Systems*** 61 Hongjing Zhang, Jun Imaoka, Mostafa Noah, Yuki Ishikura and Masayoshi Yamamoto Department of Electrical Engineering, Nagoya University, Nagoya, Aichi, Japan
B3-3	***Study and Analysis of Robust Control Technology and Wide Bandgap Power Device-Based Power Converters*** 65 En-Chih Chang[1], Chenxi Meng[2] [1]Department of Electrical Engineering, I-Shou University, Kaohsiung City, Taiwan [2]Smart Grid, College of Automation and Electronic Engineering, Qingdao University of Science and Technology, Qingdao, Shandong, China
B3-4	***Optimizing performance of a pulsed laser diode driver based on a GaN FET*** 69 John S. Glaser Efficient Power Conversion, El Segundo, CA, USA

Technical Sessions (Poster Sessions)

12:40-13:00 ***Friday, May 24***

Outside Conference Room	**Poster Session 1** Chairs: Prof. Kuo-Yuan Lo, *National Kaohsiung University of S&T, Taiwan* Prof. Ching-Jan Chen, *National Taiwan University, Taiwan*
P1-1	***Study of Passivation Layer on Bevel Edge Termination for SiC RSD*** 82 Ziyue Wang, Lin Liang, Ludan Zhang Huazhong University of Science and Technology
P1-2	***Simultaneous Formation of Ni/Ti/Al/Ag Ohmic Contacts to both p- and n- type for 4H-SiC RSD*** 86 Xiaoxue Yan, Lin Liang, Ludan Zhang Huazhong University of Science and Technology
P1-3	***Performance Analysis of Coils for Wireless Power Transfer*** N/A ChiaWei Chu, Yao-Ching Hsieh National Sun-Yat Sen University
P1-4	***Current Collapse Suppression in AlGaN/GaN HEMTs Using Silicon Substrate Removal Technique*** 91 Yueh-Ting Chen, Jian-Jang Huang National Taiwan University
P1-5	***Synchronous Rectification Method for High Frequency CLLC Resonant Converter*** 94 HwaPyeong Park, Hyunjun Choi, JeeHoon Jung Ulsan National Institute of Science and Technology
P1-6	***LV SiC MOSFETs Fabricated using HV DMOS Process*** N/A Chih-Fang Huang$_1$, Cheng-Yi Jiang$_1$, Jia-Ching Hung$_1$, Tian-Li Wu$_2$ $_1$National Tsing Hua University $_2$National Chiao Tung University
P1-7	***Paralleled-GaN based three-phase inverter with overcurrent suppression*** 98 Zu-Bin Wong, Yaow-Ming Chen National Taiwan University
P1-8	***High-Performance GaN-Based Interleaved Clamp-Switch TCM Buck Converter for a Redundant Power Module*** N/A Yu-Chen Liu$_1$, Bing-Siang Huang$_2$, Xin-Han Lin$_2$, Huang-Jen Chiu$_2$ $_1$National Ilan University $_2$Nation Taiwan University of Science and Technology
P1-9	***A modified Small-Signal Model with consideration of Leaky Buffer for GaN- on-Si HEMT*** N/A Yung Ting Ho, Kai Chieh Hsu, Yu Li Ho, Li Cheng Chang, Chao Hsin Wu National Taiwan University
P1-10	***Ohmic Contact Annealing Test for GaN*** N/A Yu-Li Ho, Chao-Hsin Wu National Taiwan University
P1-11	***Some novel designs of P implanted regions for junction barrier Schottky diode*** N/A Jhen-Min Hong, Yun-Kai Lai, Chen-Dong Tzou, Kung-Yen Lee National Taiwan University

Outside Conference Room	**Poster Session 2** Chairs: Prof. Hung-Liang Cheng, *I-Shou University, Taiwan* Prof. Chun-An Cheng, *I-Shou University, Taiwan*
P2-1	***SiC based Three-Level NPC Converter operated by Predictive Control Method with NP Voltage Balance by Offset Voltage Injection*** N/A Eun-Su Jun, Sangshin Kwak Chung-ang University
P2-2	***Energy Saving in Smart Grid*** 104 Yi-Kuan Ke National Taiwan Ocean University
P2-3	***Design and Implementation of a GaN-Based Diagnostic System for Lithium Battery Module*** N/A Chang-Hua Lin, Guan-Jung Chen, You-Lin Li National Taiwan University of Science & Technology
P2-4	***Two Stage Scheduling of Microgrid Under Spot Market Based on CVaR*** N/A Gao Rui South China University of Technology
P2-5	***Linearity of AlGaN/GaN HEMTs with Different Gate-to-Source Length*** 110 Yinan Zhong, Yue-Ming Hsin National Central University
P2-6	***Single Phase AC-AC Solid State Transformer based on Single Conversion Stage*** 113 Arief Rahman, Sheng Kai Chen, Huang Jen Chiu National Taiwan University of Science and Technology
P2-7	***Experimental Verification of DC to Single-phase AC converter with Power Decoupling Capability using 1.2 kV SiC-MOSFET Module*** 118 Hiroki Watanabe$_1$, Jun-ichi Itoh$_1$, Akio Iwabuchi$_2$ $_1$Nagaoka University of Technology $_2$Sanken Electric Co.,Ltd
P2-8	***Development of a Novel Battery-Powered DC-AC System*** 122 Ching-Ming Lai$_1$, Jiashen Teh$_2$, Yuan-Chih Lin$_3$, Claus Hsu$_4$ $_1$National Chung Hsing University $_2$Universiti Sains Malaysia $_3$National Taiwan University $_4$UPE-Power Technology Co., Ltd
P2-9	***High Switching Frequency TCM Control for Bidirectional Interleaved Buck Converter Without Phase Error for Battery Charging*** N/A Yu-Chen Liu$_1$, Yong-Long Syu$_2$, Kai-De Chen$_2$, Chen Chen$_2$, Katherine A Kim$_3$, Huang-Jen Chiu$_2$ $_1$National Ilan University $_2$National Taiwan University of Science and Technology $_3$National Taiwan University
P2-10	***Voltage Sharing Control Strategy of Chargers Connected in Series under Constant Current Mode*** N/A Elia Huang, Huang-Jen Chiu, Pao-Sheng Huang National Taiwan University of Science and Technology
P2-11	***Improvement of Charging Technology on GEL Battery and Design Analysis of Charger*** N/A Chang-Han Xie, Kung-Yen Lee, Bin-Juine Huang National Taiwan University