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"Modeling and Simulation of FinFET and Nanosheet Transistors for Advanced Technology Nodes"...N/A

Yogesh Chauhan

Indian Institute of Technology Kanpur, India

"Hexagonal boron nitride based electronic devices and circuits: status and prospects"...N/A

Mario Lanza

King Abdullah University of Science and Technology (KAUST), Saudi Arabia

"From CMOS to neuromorphic Computing, with a peek into the future"...N/A

Merlyne De Souza

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"Heterogeneous Integration for AI Architectures"...N/A

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IBM-USA

"Quantum Computing in Nanoscale CMOS using Position-Based Charge Qubits"...N/A

Elena Blokhina and Robert Bogdan Staszewski

University College Dublin, Ireland

Panel - SBMicro 2021

“Product Development - Open Labs or MPW foundry runs”...N/A

Moderators: Davies William de Lima Monteiro and Roberto Panepucci

Romano Hoofman - imec, Belgium

program management of EUROPRACTICE and related innovation services

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User Program Manager at the Cornell NanoScale Science and Technology Facility (CNF) of the NNCI - National Nanotechnology Coordinated Infrastructure

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Flavio Orlando Plentz Filho - Univ Federal de Minas Gerais, Brasil

Professor Associado IV da Universidade Federal de Minas Gerais

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<p>CMOS Technology Enabled Physically Flexible-Stretchable-Spherical Solar Cells...N/A Muhammad Mustafa Hussain, University of California, Berkeley</p>