2024 IEEE Computer Society Annual Symposium on VLSI (ISVLSI 2024)

Knoxville, Tennessee, USA
1-3 July 2024

IEEE Catalog Number: CFP24179-POD
ISBN: 979-8-3503-5412-6

****** This is a print representation of what appears in the IEEE Digital Library. Some format issues inherent in the e-media version may also appear in this print version.***

IEEE Catalog Number:	CFP24179-POD
ISBN (Print-On-Demand):	979-8-3503-5412-6
ISBN (Online):	979-8-3503-5411-9
ISSN:	2159-3469

Additional Copies of This Publication Are Available From:

Curran Associates, Inc
57 Morehouse Lane
Red Hook, NY 12571 USA
Phone: (845) 758-0400
Fax: (845) 758-2633
E-mail: curran@proceedings.com
Web: www.proceedings.com

2024 IEEE Computer Society Annual Symposium on VLSI (ISVLSI)

ISVLSI 2024

Table of Contents

INVITED TALK

TECHNICAL SESSION 1A: CIRCUITS, RELIABILITY, AND FAULT-TOLERANCE

TECHNICAL SESSION 1B: COMPUTER-AIDED DESIGN AND VERIFICATION

TECHNICAL SESSION 2A: DIGITAL CIRCUITS AND FPGA-BASED DESIGNS I

TECHNICAL SESSION 2B: EMERGING AND POST-CMOS TECHNOLOGIES I

TECHNICAL SESSION 3A: VLSI FOR APPLIED AND FUTURE COMPUTING I

TECHNICAL SESSION 4A: SYSTEM DESIGN AND SECURITY I

TECHNICAL SESSION 5A: SYSTEM DESIGN AND SECURITY II

TECHNICAL SESSION 5B: VLSI FOR APPLIED AND FUTURE COMPUTING II

TECHNICAL SESSION 6A: DIGITAL CIRCUITS AND FPGA-BASED DESIGNS II

TECHNICAL SESSION 6B: VLSI FOR APPLIED AND FUTURE COMPUTING II

TECHNICAL SESSION 7A: EMERGING AND POST-CMOS TECHNOLOGIES II

SPECIAL SESSION 1: HARNESSING THE POWER OF TRUSTED AI IN IOT EDGE/CLOUD SYSTEM

SPECIAL SESSION 2: ASSURED AND TRUSTED SEMICONDUCTOR MICROELECTRONICS INTEGRATED CIRCUITS (ICS)

SPECIAL SESSION 3: NEUROMORPHIC AND EDGE COMPUTING

SPECIAL SESSION 4: HARMONIZING HARDWARE SECURITY WITH EMERGING TECHNOLOGIES

SPECIAL SESSION 5: EMERGING TOPICS IN HARDWARE SECURITY: FROM LLM TO HETEROGENEOUS INTEGRATION

SPECIAL SESSION 6: EMERGING DEVICES IN MACHINE LEARNING ACCELERATION

SPECIAL SESSION 7: EMERGING FRONTIERS IN CPS AND IOT SECURITY

SPECIAL SESSION 8: EMERGING SENSING, COMPUTING, AND TELEMETRY FOR IOT EDGE DEVICES

SPECIAL SESSION 9: ROBUSTNESS OF EDGE COMPUTING ENVIRONMENT

SPECIAL SESSION 10: SUSTAINABLE COMPUTING FROM EDGE TO DATA CENTER

SPECIAL SESSION 11: SMART CYBER-PHYSICAL SYSTEMS

SPECIAL SESSION 12: FRONTIERS OF COMPUTING ARCHITECTURE AND SYSTEM DESIGN WITH BEYOND MOORE DEVICES

QUANTUM WORKSHOP 1

QUANTUM WORKSHOP 2

QUANTUM WORKSHOP 3

POSTER SESSION 1: VLSI FOR APPLIED AND FUTURE COMPUTING

POSTER SESSION 1: COMPUTER-AIDED DESIGN AND VERIFICATION

POSTER SESSION 1: CIRCUITS, RELIABILITY, AND FAULT-TOLERANCE

POSTER SESSION 1: EMERGING AND POST-CMOS TECHNOLOGIES AND SPECIAL SESSIONS

POSTER SESSION 2: DIGITAL CIRCUITS AND FPGA-BASED DESIGNS

POSTER SESSION 2: SYSTEM DESIGN AND SECURITY

POSTER SESSION 2: LATE BREAKING RESEARCH

POSTER SESSION 2: STUDENT RESEARCH FORUM

POSTER SESSION 2: SPECIAL SESSIONS